

An Early History of Optimization Technology for Automated Design of Microwave Circuits

JOHN W. BANDLER ¹ (Life Fellow, IEEE) AND JOSÉ ERNESTO RAYAS-SÁNCHEZ ² (Senior Member, IEEE)

(Invited Paper)

¹Department of Electrical and Computer Engineering, McMaster University, Hamilton, ON L8S 4K1, Canada

²Department of Electronics, Systems and Informatics, ITESO – the Jesuit University of Guadalajara, Tlaquepaque 45604, Mexico

CORRESPONDING AUTHOR: John W. Bandler (e-mail: bandler@mcmaster.ca).

This work was supported in part by the National Research Council of Canada, in part by the Natural Sciences and Engineering Research Council of Canada, in part by McMaster University, in part by Optimization Systems Associates Inc., in part by Bandler Corporation, in part by the ITESO – The Jesuit University of Guadalajara, and in part by the Consejo Nacional de Ciencia y Tecnología, Mexican Government.

ABSTRACT This paper outlines the early history of optimization technology for the design of microwave circuits—a personal journey filled with aspirations, academic contributions, and commercial innovations. Microwave engineers have evolved from being consumers of mathematical optimization algorithms to originators of exciting concepts and technologies that have spread far beyond the boundaries of microwaves. From the early days of simple direct search algorithms based on heuristic methods through gradient-based electromagnetic optimization to space mapping technology we arrive at today’s surrogate methodologies. Our path finally connects to today’s multi-physics, system-level, and measurement-based optimization challenges exploiting confined and feature-based surrogates, cognition-driven space mapping, Bayesian approaches, and more. Our story recognizes visionaries such as William J. Getsinger of the 1960s and Robert Pucel of the 1980s, and highlights a seminal decades-long collaboration with mathematician Kaj Madsen. We address not only academic contributions that provide proof of concept, but also indicate early formative milestones in the development of commercially competitive software specifically featuring optimization technology.

INDEX TERMS Adjoint sensitivities, Bayesian, Broyden, CAD, cognition-driven design, design centering, electromagnetics-based design, empipe, FDTD, FEM, Huber, least p th, microwave design, MIMIC, minimax, MTT 70th Anniversary Special Issue, neural networks, nonlinear modeling, optimization, space mapping, surrogate modeling, TLM, tolerance optimization, tuning, waveguide filters, yield-driven design.

I. INTRODUCTION

The first numerical optimization techniques for circuit design emerged in the 1960s [1], [2], [3], [4], including pioneering optimization methods for RF and microwave passive filter design. A key visionary, W. J. Getsinger, founded in 1968 the IEEE MTT-S Technical Committee on Computer-Aided Design of Microwave Circuits, known as “MTT-1” (see Fig. 1). In 1969, Getsinger (see Fig. 2) also edited the special issue of *IEEE Trans. Microwave Theory and Techniques on Computer-Oriented Microwave Practices* [3], featuring the first microwave paper to review optimization [4]. This paper opens with “Fully automated design and optimization is surely one of the ultimate goals of computer-aided design.” After

more than five decades, has this goal been achieved yet? It depends on what is meant by “fully automated” [5].

In those early days, experts in circuit theory and electromagnetics were convinced that a “feel” for a problem surpassed any advantage offered by the emerging use of digital computers. The wisdom was that an expert’s “feel” defied automation [5]. Both academics and practitioners even objected to the use of digital computers for algorithmic circuit design, as did John Roberts, Bandler’s Ph.D. advisor at Imperial College London in 1963. Some influential electrical engineers judged computer-aided design (CAD) and optimization as “not engineering” and unsuited for engineering education [5]. Still, pioneering optimization contributions

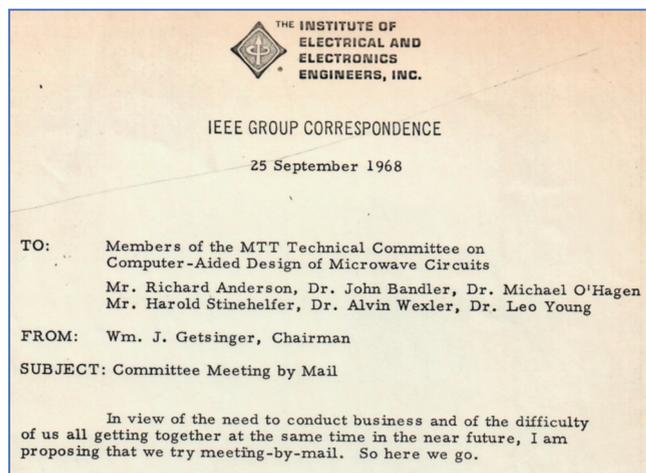


FIGURE 1. Initial part of a letter sent in 1968 by Getsinger to the founding members of the MTT-S Technical Committee on CAD of Microwave Circuits, in the year of its creation.

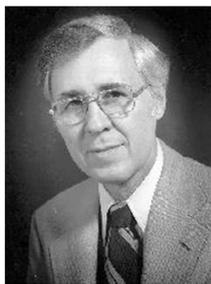


FIGURE 2. William J. Getsinger in 1969, guest editor of the special issue on Computer-Oriented Microwave Practices of the IEEE Transactions on Microwave Theory and Techniques [3].



FIGURE 3. Mathematician Kaj Madsen in 1973; Technical University of Denmark, Copenhagen, collaborator for over 4 decades and architect of optimization algorithms and software.

were already in progress in the 1960s [1], [4], mostly based on heuristic search methods with no derivative information [5].

The first gradient-based optimization methods, mostly quasi-Newton approaches, were rapidly adopted in the 1970s [4]. By that time, automatic optimization was already seen as the most significant advance in microwave CAD [6].

During the 1970s and 1980s, mathematician Kaj Madsen (see Fig. 3) and his research group at the Technical University of Denmark [7] developed powerful minimax and related mathematically rigorous algorithms, consolidating

quasi-Newton gradient methods for circuit design optimization. Co-author Bandler was fortunate to establish a decades-long relationship with Madsen and his team.

Nominal, worst-case, statistical, and multi-objective circuit optimization was already in place [8], [9]. Microwave statistical design and worst-case tolerance optimization also started in the 1970s [10]. Initial optimization cases in high-dimensional design spaces were developed in the 1980s for satellite waveguide multiplexers [11]. The first gradient-based direct electromagnetic design optimization method, tailored to microwave filters, appeared in the early 1990s [12].

In 1993, recognizing the urgent need for effective electromagnetic optimization, while puzzling over the mystery of the so-called engineer’s “feel” for a problem and the cognitive concept of “model,” and after visiting Madsen in Denmark, Bandler invented space mapping [13], [14], [15], [16].

Driven also by the need for optimizing expensive-to-compute functions, mathematicians developed their own surrogate approaches in the same decade [17]. On a separate track but in those same years, artificial neural networks (ANN) for RF and microwave design optimization emerged [18], [19]. Prominent applications to microwave design optimization of space mapping [20], surrogate modeling [21], ANN [22], [23], and combinations of them [24], [25], [26], [27], [28], [29] flourished in the 2000s and 2010s.

The last two decades have confirmed the prediction made in 2001 [30], that knowledge-based techniques would dominate in addressing current and future microwave design optimization challenges [31]. Today’s thinking about current and future trends in microwave design automation are summarized in [32].

This paper briefly tracks the early history of optimization methods and techniques for RF and microwave design. It highlights the main milestones in several overlapping chronological stages of microwave design optimization: 1) circuit-oriented optimization methods; 2) direct electromagnetic optimization techniques; 3) space mapping optimization and surrogate approaches; 4) surrogate methodologies and Bayesian approaches; and 5) cognition-driven design.

We also highlight the impact of the MIMIC (Microwave/Millimeter-Wave Monolithic Integrated Circuits) Program and a vibrant decade—mid 1980s to mid 1990s—during which intense theoretical contributions and commercial software innovations became widely accepted by microwave engineers. Co-author Bandler’s optimization-focused company, Optimization Systems Associates, founded in 1983, acquired by Hewlett-Packard in 1997 [33], flourished center stage during that formative period in the history of microwave optimization technology.

II. CIRCUIT-ORIENTED MICROWAVE OPTIMIZATION

Successful numerical optimization methods for microwave design were first applied to equivalent lumped and distributed circuit models [4]. Multidimensional direct search strategies relying exclusively on objective function values, such as pattern search [4], Nelder-Mead’s simplex [34], and razor search

[35], were initially adopted. These were soon replaced by gradient-based methods when accurate and computationally efficient derivatives became available.

A. GRADIENT-BASED OPTIMIZATION WITH ADJOINT SENSITIVITIES

The breakthrough adjoint sensitivity technique allows the calculation of accurate response derivatives with respect to design parameters at a low computational cost. Director and Rohrer's milestone [36] for lumped circuit adjoint sensitivities in 1969 was extended in the 1970s to distributed circuits [37], and to first- and second-order sensitivities for wave variables [38]. The adjoint sensitivity technique requires at most two full circuit simulations, one of the original circuit and one of an appropriately excited adjoint circuit, regardless of the number of design parameters.

CAD vendors paid little attention to adjoint sensitivities in the subsequent two decades, mainly due to complications of implementation and unclear market interest [5]. In contrast, response surface modeling and interpolation were easier to implement. Nevertheless, powerful circuit-oriented adjoint network gradient-based nonlinear optimization methods were already available in the 1980s [40]. Renewed interest in adjoint sensitivities emerged as CAD moved towards electromagnetics-oriented design optimization [41], as described in Section III.

B. LEAST PTH AND MINIMAX OBJECTIVES AND ALGORITHMS

Another breakthrough during the 1970s and 1980s was the emergence of effective least p th and minimax algorithms for design optimization [39]. The generalized least p th objective (H_p) that played a significant role in microwave design [40], [42], and later paved the way to design centering as well as to active device modeling and parameter extraction [5] is given by

$$H_p(\mathbf{e}) = \begin{cases} H_p^+ = \left[\sum_{j \in J} |e_j|^p \right]^{1/p} & \text{if } J \text{ is not empty} \\ H_p^- = \left[\sum_{j=1}^m (-e_j)^{-p} \right]^{-1/p} & \text{otherwise} \end{cases} \quad (1)$$

where $J = \{j \mid e_j \geq 0\}$ defines the set of indices for which the response error functions violate the specifications. In other words, if at least one error e_j is nonnegative (at least one spec violation), H_p^+ is used; if all specs are satisfied, H_p^- is used.

These optimization methodologies found application in commercial software in the 1980s, 1990s and beyond.

C. YIELD-DRIVEN MICROWAVE DESIGN

Pioneering work on yield-driven design—also called statistical design, design-centering, or design with tolerances and uncertainties—emerged in the 1970s [8], [43], [44], including uncertainties not only in models but also in parasitic

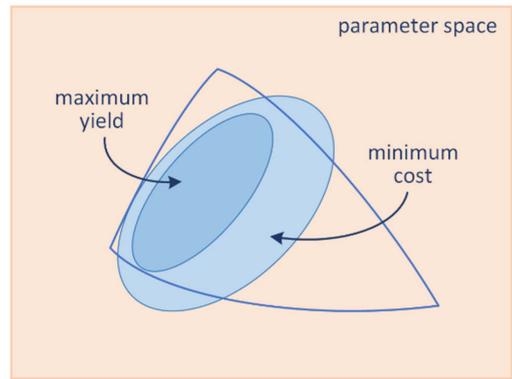


FIGURE 4. Two regions of design parameter values statistically generated around a maximum yield design and a minimum cost design, respectively; the region of acceptable designs is enclosed by the thin curved lines [40]. The corresponding nominal designs could be anywhere within those regions, depending on the constraints, tolerances, and probability distribution function of each individual design variable.

effects and basic environmental conditions [45]. The fundamental concept is illustrated in Fig. 4 [40], where we see a parameter space of design variables with a region of acceptable designs, and statistically generated outcomes around two nominal designs. The aim is to “center” a design with its possible outcomes to maximize yield or minimize cost.

Throughout the 1980s and 1990s, industrially implementable optimization algorithms were developed for yield-driven design using one-sided L_1 [40], [46], one-sided Huber formulations [47], and a family of advanced L_1 , L_2 , and minimax optimizers, with both exact and approximate gradients [48]. L_2 (least squares or Euclidean norm, $p = 2$) is very sensitive to gross errors (outliers affect its performance significantly), while L_1 (L_1 -norm, 1-norm, or Manhattan norm, $p = 1$) is more robust against “wild” data but is biased by small errors [46]. Huber is a hybrid of these two [49], treating large errors in the L_1 sense and small errors in the L_2 sense. Given its properties to deal with bad starting points, robustness and consistency in the presence of large and small errors, Huber proved extremely effective for statistical device modeling, analog fault location, and design centering [47], [50].

D. MINIMAX OPTIMIZATION

Based on prior work by mathematician Kaj Madsen [7], [51], [52], [53] and his team at the Technical University of Denmark, minimax (equal-ripple) optimization of microwave equivalent circuits [54] was successfully applied during the 1980s to diverse design application cases.

Representative minimax design optimization examples include: impedance transformers, interdigital filters, and microwave reflection amplifiers [51]; waveguide manifold multiplexers with up to 240 optimization variables following an automatic decomposition algorithm (see Fig. 5 [11]); and five-channel multiplexers optimized by weighted updates for gradient approximations (see Fig. 6 [55]).

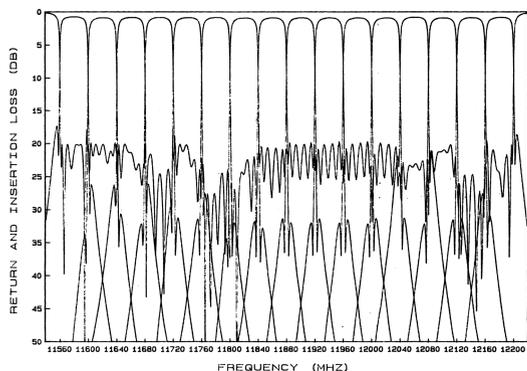


FIGURE 5. Optimized 16-channel waveguide multiplexer using 240 optimization variables. From [11].

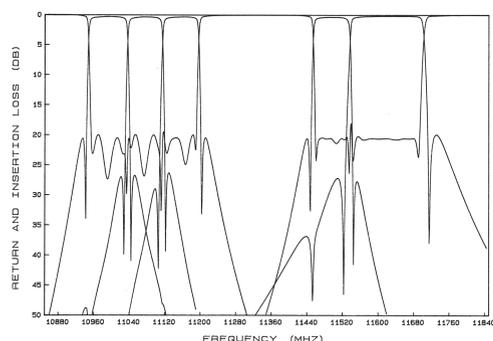


FIGURE 6. Optimized 5-channel noncontiguous waveguide multiplexer using 75 optimization variables. From [55].

In essence, a (so-called one-sided) minimax formulation to design optimization consists of solving

$$\mathbf{x}^* = \arg \min_{\mathbf{x}} U(\mathbf{x}) = \arg \min_{\mathbf{x}} \max_k \{ \dots e_k(\mathbf{x}) \dots \} \quad (2)$$

where \mathbf{x}^* is the optimal design and U is the objective function consisting of the maximum error in a set of errors. For each response of interest (S-parameters, output power, etc.) and independent variable sample (frequency, time, temperature, etc.), an error function is defined such that a negative value in the k th error function, $e_k(\mathbf{x})$, implies that the corresponding design specification is satisfied, otherwise it is violated. If $U(\mathbf{x}^*) < 0$, the optimal design satisfies all the specifications. If $U(\mathbf{x}^*) \geq 0$, at least one of the design specifications is being violated at the optimal design found.

E. NONLINEAR CIRCUIT DESIGN OPTIMIZATION

The 1980s saw significant progress in nonlinear microwave circuit simulation, intermodulation, frequency conversion, stability, noise analysis, sensitivity analysis, and optimization, exploiting the harmonic balance technique [56]. A fruitful Bandler-Rizzoli scientific synergy [57] was established for developing CAD tools with nonlinear design automation capabilities [58], [59].



FIGURE 7. Rautio (right) with Bandler (left) during the IMS-2019, where Rautio was honored by the Microwave Career Award.

Linking state-of-the-art optimization and efficient harmonic balance simulation was key to developing powerful nonlinear microwave circuit design optimization approaches in the early 1990s. The formidable problem of yield-driven optimization of nonlinear microwave circuits operating in the steady state under large signal periodic excitations was tackled by unifying FAST (Feasible Adjoint Sensitivity Technique: exact adjoints combined with perturbations) and DC, small-signal, and large-signal simulations [60]. Yield-driven microwave active device modeling and circuit design was achieved [61] by integrating physics-based MESFET models with harmonic balance simulation and optimization. Commercial CAD tools were developed for robust FET model measurement-based parameter extraction exploiting L_1 , L_2 , and minimax optimizers [62], later generalized to a nonlinear device characterization by statistical modeling enhanced by FAST and Huber optimizers [63].

III. DIRECT ELECTROMAGNETIC MICROWAVE OPTIMIZATION

Putting full-wave electromagnetic simulators into optimization loops was infeasible—laughable to many—in the 1980s and early 1990s given the computational cost implied. It simply seemed absurd to electromagnetics simulation experts [5], both academic and industrially oriented. They could not envisage HP’s HFSS [64] and Ansoft’s Maxwell Eminence [65] as ever being suited to direct optimization. Their vision proved wrong. The first direct, gradient-based electromagnetic optimization process applied to microwave filter design was published in 1993 [12]. It exploited response surface modeling, smooth gradient estimation, and data-base updates to overcome the computational challenges [66].

OSA’s methodology for automated capture of structural, geometrical and material parameters in “external” simulators was key to enable direct electromagnetic optimization. In 1992, after a cordial meeting with Sonnet founder and CAD visionary Jim Rautio (see Fig. 7), Sonnet Software becomes the first commercial electromagnetic simulator vendor to offer its flagship software *em*TM to OSA specifically

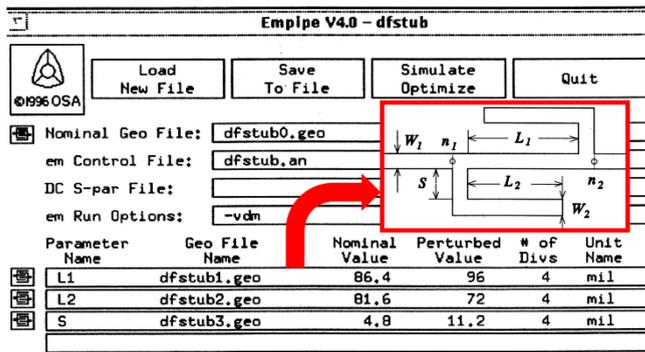


FIGURE 8. A 1996 Empipe™ Geometry Capture user interface setup for direct electromagnetic optimization of a double folded stub microstrip filter in Sonnet Software’s *em*™ [69].

for design optimization purposes. Developed in 1992, OSA’s Empipe™ [67] was the first commercially available software for such automated parameterization through its exclusive Datapipe [68] and Geometry Capture [69] technologies. Empipe empowered industrial optimization algorithms [59] with parametric encapsulation and control of Sonnet Software’s planar tool *em*™ [70] in iterative optimization loops (see Fig. 8). For the first time also it became possible, using OSA90/hope with its one-sided L_1 algorithm [40], [46], to perform direct gradient-based yield optimization of circuits with components or subcircuits simulated by an electromagnetic simulator [71].

A parameterization scheme of 3D structures specifically tailored to Hofer’s TLM electromagnetic field simulation and optimization was achieved in 1993 [72], [73].

Later, in 1996, after Hewlett-Packard and Ansoft finally granted OSA access to their flagship electromagnetic simulators, OSA released Empipe3D™ [74], [75]. Empipe3D enabled automated parametric design encapsulation of arbitrary 3D structures by processing the HFSS project files in HP’s HFSS™ [76] and Ansoft Corporation’s Maxwell Eminence™ [77], allowing direct 3D electromagnetic optimization [78]. Once a structure was captured, Empipe3D automatically generated the appropriate projects for HFSS, invoked the field solver and processed the solution files to display and optimize various responses.

A method for unrestricted shape optimization tailored to 3D FDTD EM simulation became available in 1998 [79].

Subsequent developments for direct electromagnetic optimization include gradient-based methods using adjoint sensitivities. Initial “exact” sensitivities based on analytical derivatives of relevant FEM system matrices to compute S-parameter sensitivities [80] were implemented in a commercial FEM solver [81], [82]. Specific adjoint-style approaches are now commercially implemented [83], [84]. Promising exact analytical expressions for field-based sensitivities, not specific to any particular electromagnetic analysis technique or meshing strategy, are now available [85].

A more recent strategy to accelerate direct electromagnetic optimization consists of internally modifying the analysis method (FEM) to incorporate the optimization algorithm as part of the simulation execution [86], such that both, the electromagnetic simulation and design optimization, are finished at the same time [87], [88].

IV. COMMERCIAL OPTIMIZATION TOOLS: THE DECADE OF ACCEPTANCE

Until the early 1980’s, iterative computer-oriented methods for microwave circuit optimization were regarded as curiosities, not taken seriously by either influential circuit theory “synthesis” purists or “nuts-and-bolts” engineers [16]. Moreover, because the then optimization test examples were often simple, frequency-domain, linear circuits with just a handful of optimization variables, proposed algorithms were looked upon as impractical, lacking in mathematical rigor, and incapable of solving industrially meaningful design problems. However, the use of digital computers to analyze complex circuits, linear and nonlinear, and to validate structures through electromagnetic simulation was never questioned no matter how daunting. In essence, computers were typically thought of as ultrafast electronic calculators. The prospect that a computer could somehow “create” an optimal design by repeated analysis driven by a simplistic, seemingly trial-and-error algorithm must have been frightening and, in the electromagnetics realm, quite unthinkable. The decade from the mid 1980s to the mid 1990s was a major turning point in design optimization technologies for RF and microwave engineering.

A. THE IMPACT OF THE MIMIC PROGRAM ON COMMERCIAL MICROWAVE CAD TOOLS

A key catalyzer for the accelerated evolution of commercial EDA tools for microwave optimization, and their wide acceptance in industry, was the 7-year, \$0.5-billion U.S. Department of Defense MIMIC (Microwave/Millimeter-Wave Monolithic Integrated Circuits) Program, initiated in 1986 [89]. To reach its goals, the MIMIC program aimed at “substantially improving computer-aided design models and tools” [89], providing resources and structure to make possible the transition from design automation research to efficient and affordable MIMIC manufacturable production in gallium arsenide technology for a wide variety of applications [90], both military and commercial [91].

Key players in the MIMIC program participating in the industry consortia focused on CAD problems included Compact Software (in association with OSA [16], [92]), EEsof, and Sonnet Software, headed by joint ventures between the major contractors: Raytheon and Texas Instruments, and ITT and Martin Marietta [90], [91].

The MIMIC Program detonated fast developments for wafer/chip yield prediction and optimization, including state-of-the-art device statistics, measurement uncertainties, worst

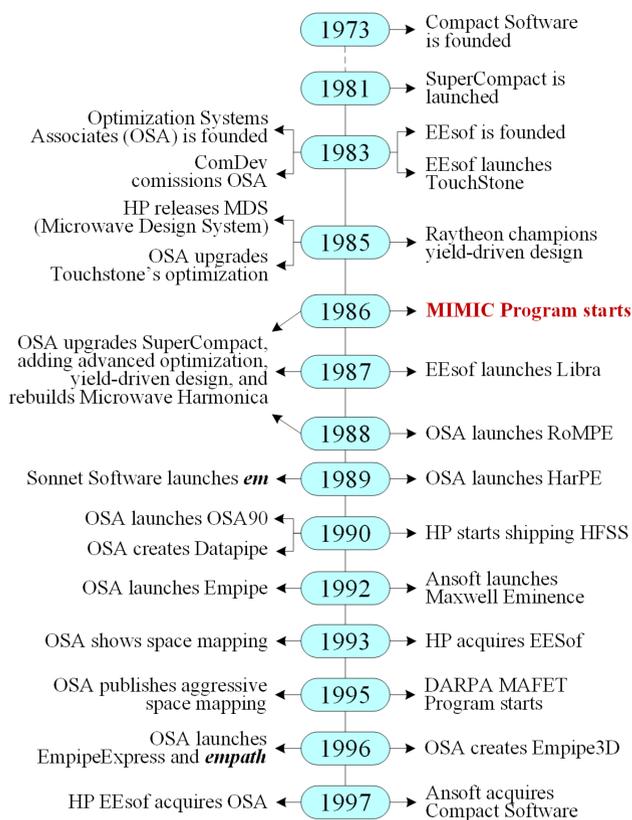


FIGURE 9. A timeline of major commercial events in RF and microwave optimization technology (early 1980s to late 1990s).

case design, design centering, and tolerance assignment for linear and nonlinear circuits.

B. TIMELINE OF MAJOR COMMERCIAL MICROWAVE OPTIMIZATION-ORIENTED EVENTS: EARLY 1980'S TO LATE 1990'S

The following is a brief, personal timeline of major commercial advances focusing on optimization technology in the microwave arena. Contributors in important, related areas of design automation are omitted in this brief sketch that specifically emphasizes optimization (see Fig. 9).

The period from the early 1980s to the late 1990s is marked by setbacks, surmounting barriers of disbelief, triumphs, corporate acquisitions, and the movement of experts and expertise between vendors, all ultimately benefitting advances in RF and microwave design and manufacturing technology.

CAD visionary Les Besser comes onto the scene early, puts his experience into practice, founds Compact Software in 1973 [93], and his commercial circuit design offering COMPACT features optimization algorithms.

The next generation of COMPACT (Computerized Optimization of Microwave Passive and Active CircuiTs) is named SuperCompact (1981).

Optimization Systems Associates Inc. (OSA) is founded (1983). Canadian satellite telecommunications company

ComDev commissions OSA (1983) to introduce advanced optimization technology to its in-house flagship waveguide manifold multiplexer design portfolio.

Former Compact Software developer Bill Childs co-founds EEsof in 1983 and creates Touchstone (1983) to compete with Compact Software's SuperCompact. Touchstone runs from the outset on the PC.

HP releases MDS (Microwave Design System) in 1985 [94].

OSA contributes advanced optimization capabilities to EEsof's Touchstone (1985). But EEsof rejects Bandler's pitch for yield-driven design as a follow-up to OSA's upgrade of Touchstone's optimization options based on EEsof's conviction that their customers would never ask for such capability. Of course, after OSA later delivers this capability to Compact Software, EEsof scrambles to follow suit.

The imminent MIMIC program motivates Bob Pucel for Raytheon Research Division in 1985 to champion yield-driven design and brings Bandler and OSA together with Compact Software.

EEsof's Libra, featuring harmonic balance simulation, competes with Compact Software's corresponding product Microwave Harmonica (1987).

OSA is commissioned to re-engineer SuperCompact and introduces advanced optimization and yield-driven design (1986–1988), also entirely new documentation to Compact Software's premier products, and then re-builds Microwave Harmonica (1987–1988).

OSA creates active device parameter extraction software RoMPE (1988) and HarPE (1989), and general purpose CAD software OSA90 (1990) and OSA90/hope (1991).

OSA creates Datapipe technology (1990) to drive external simulators such as Zuberek's SPICE-PAC (1992) [95], Sonnet's *em* (1992) [70], and Hofer's TLM (1993) [72].

Sonnet Software starts shipping *em* in 1989 [96].

Ansoft creates 3D electromagnetic simulator HFSS for HP; HP starts shipping HFSS in 1990; Ansoft markets its own competing product Maxwell Eminence in 1992.

OSA creates Empipe (1992), EmpipeExpress [97] (1996) and *empath* [98] (1996).

HP acquires EEsof (1993), hence the name HP EEsof.

OSA demonstrates space mapping (1993), aggressive space mapping (1995).

OSA unsuccessfully proposes an ambitious optimization-oriented project (1995) for the DARPA MAFET Program [99], [100].

HP and Ansoft finally make their 3D simulators available to OSA and OSA creates Empipe3D (1996) to drive HP HFSS and Ansoft's Maxwell Eminence. Some backstory: after Dan Swanson (see Fig. 10) spoke highly of the Empipe technology during a meeting, a key member of Ansoft vowed to have Maxwell Eminence on OSA's doorstep ("Within two weeks or I'll eat my business card").

HP EEsof (later HP spinoff Agilent Technologies, now Keysight Technologies) acquires OSA [101] (1997) and the



FIGURE 10. Dan Swanson (right) and Radek Biernacki (left) at the OSA exhibition booth during the IMS-1994 in San Diego, CA.



FIGURE 11. Steve Chen at the OSA exhibition booth during the IMS-1995 in Orlando, FL.

Empipe family becomes the foundation of Agilent HFSS Designer and Momentum Optimization.

Ansoft acquires Compact Software (1997). In 2001, Ansoft buys HP's HFSS product line. Ansys acquires Ansoft (2008).

C. OPTIMIZATION SYSTEMS ASSOCIATES

The longest in-person professional associations with Bandler are those by Steve Chen (see Fig. 11) and Radek Biernacki (see Fig. 10). They were key creative members of Optimization Systems Associates until HP's acquisition and OSA's products were absorbed into HP EEs of in 1997. Biernacki and Chen relocated to HP EEs of in 1997 [102].

When faced with OSA's collapse in relations with Compact Software, Raytheon, Texas Instruments, and the MIMIC Program in 1989, Chen and Biernacki vowed to continue working with Bandler and OSA "to the bitter end." Without their creativity, dedication, initiative and intellectual skillset, the optimization-centric products, RoMPE, HarPE, OSA90,

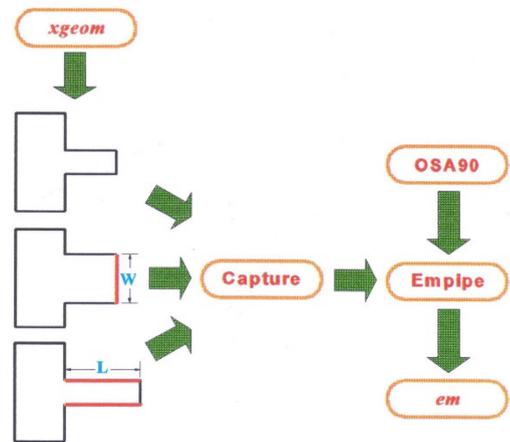


FIGURE 12. Example of Geometry Capture for parameterizing a microstrip step structure. From [105].

OSA90/hope, Empipe, Empipe3D, EmpipeExpress and *em-path* would surely never exist. And successful commercial electromagnetic optimization and the space mapping concept itself might at best have been delayed by several years. None of the RF/microwave software vendors nor any practitioners in electromagnetic simulation had any thoughts or vision for electromagnetic optimization until OSA produced results.

Why did HP acquire OSA? Importantly, OSA had successfully leveraged the premier electromagnetic simulators of HP's competitors in addition to its own HFSS. " 'Integrating OSA's technology into our electromagnetic product line will effect a paradigm shift in how these tools are used,' [Jake] Egbert said. 'Our electromagnetic tools will move from being analysis tools to design tools.'" [101].

But there may be a deeper reason. OSA's customers appreciated the full range of OSA optimization offerings. By 1997, OSA90 [68] included state-of-the-art optimization algorithms, both gradient-based (minimax, L_1 , L_2 , Huber, quasi-Newton, conjugate-gradient, yield-driven) and direct search-based (simplex, random, simulated annealing). Indeed, at the IEEE International Microwave Symposium Exhibitions, Bandler recalls the annual excitement of "what's new?" from academics and practitioners alike. Expectations were high. Competition stiff. Microwave Engineering Europe proposed CAD challenges regularly that OSA and other vendors rose to and OSA performed commendably, for example [103], [104]. The class B frequency doubler example [104] later demonstrated Geometry Capture with simultaneous electromagnetic/harmonic balance optimization [105]. See Fig. 12 (Geometry Capture) and Fig. 13 (doubler circuit).

Bandler recalls a visit with British Telecom in the U.K. and asked why OSA hadn't heard from them since British Telecom acquired OSA software. Their response, "We compare OSA device models with those of HP." "What do you do when the models disagree?" "Of course, we complain to HP."

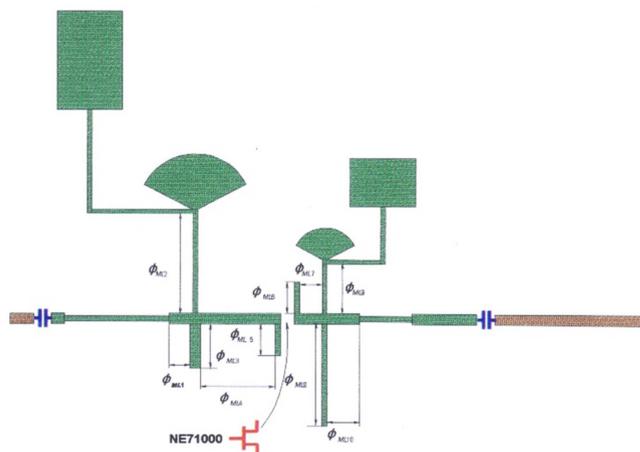


FIGURE 13. Circuit structure of a class B frequency doubler with ten geometrical optimization variables [105]. The circuit is derived from a Microwave Engineering Europe challenge [104].

V. SPACE MAPPING OPTIMIZATION AND SURROGATE APPROACHES

In 1994, space mapping [13], [14], [15] surprised the RF and microwave community. Space mapping is deeply rooted in engineering expertise and intuition [16]. In fact, as published everyday examples of space mapping demonstrate [20], [106], [107], it turns out that space mapping, in particular, aggressive space mapping, manifests itself as a mathematical expression of the engineer’s mysterious “feel” or expertise [16].

A. INITIAL REACTIONS TO SPACE MAPPING

What was the immediate reaction of Bill Childs, EEs of Vice President, in 1994 during a poster presentation at the International Microwave Symposium in San Diego [111] to Bandler’s first-ever public presentation of space mapping? Notable disbelief at its deceptive simplicity. Even outrage. On the other hand, at the same conference, when presented verbally with the gist of space mapping, filter synthesis giant Ralph Levy (early skeptic in the 1960’s and 1970’s of the utility of optimization and/or design with tolerances) grasped the significance of space mapping right away. “Yes,” he said. “I did that the other day when I needed HFSS just to calibrate a single point.” His excitement was palpable. As had been Vittorio Rizzoli’s when, prepublication, Bandler outlined space mapping through hand waving gestures in Rizzoli’s office in Bologna, Italy, months earlier [16], [112].

Right from the start Bandler saw space mapping in action everywhere. He recalls a seminar by Wolfgang Hofer during which Bandler suggested that Hofer’s design procedure constituted an example of space mapping. Hofer, horrified, said, “Everything isn’t space mapping, John.” Hofer’s technique was intuitive, iterative, and yielded good results in a handful of steps, the hallmark of space mapping.

Experienced filter designers, for example, often arrived at good design solutions seemingly without a formal, widely

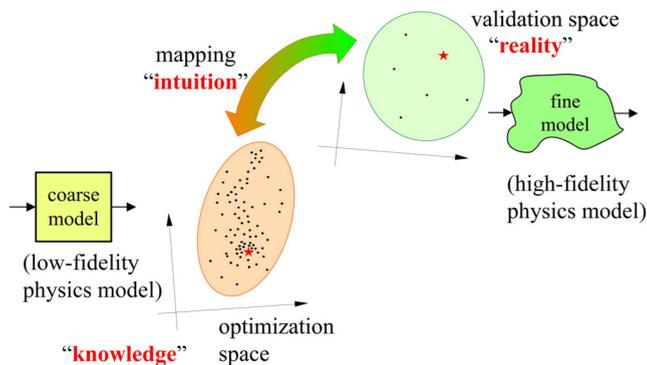


FIGURE 14. The space mapping concept discovered by Bandler in 1993, published in 1994 [13], [14], [15], [16].

understood procedure. How did they do it? Fast but approximate circuit models existed. High-fidelity electromagnetic validations were possible. But how to align the two simulation processes so that a designer could exploit the speed of the circuit model while frugally harnessing the accuracy of the electromagnetic model?

Until Bandler pioneered space mapping in the context of electromagnetic optimization, the microwave community largely adopted algorithms from outside, from mathematicians such as Fletcher and Powell [113] and later Kaj Madsen, for example, [54], [55]. (At a conference on optimization circa 1970, Fletcher remarked to Bandler, perhaps facetiously, that he never read anyone else’s papers.) From 1993 onward we began to reverse the trend, delving deeper and deeper into space mapping approaches, for example, [14], [15], [20], [24].

Bandler had struggled for 30 years to understand the so-called engineer’s “feel” for a design problem until he discovered that it was not as mysterious as claimed by practitioners. Space mapping embodies this “feel”—its simplicity and effectiveness shocked both the microwave engineering and mathematical optimization communities.

Such a simple mathematical technique covered such a wide range of design optimization problems. This led to Bandler’s conviction that the idea had been in widespread use already. Indeed, those with “expert” knowledge, like Hofer and Levy, knowingly or unknowingly, harness the space mapping concept in activities ranging from everyday human experiences to expert tuning and design of complex systems with electromagnetic accuracy.

B. THE ESSENCE OF SPACE MAPPING

To avoid performing the direct optimization of computationally expensive models (also known as fine models, or high-fidelity models), space mapping iteratively enhances or maps a coarse model (idealized or low-fidelity model) to the accuracy of the corresponding fine model. In space mapping, this coarse model is typically a physics-based quasi-global model (see Fig. 14). Space mapping mimics an expert’s intuition exploiting available, fast, parameterized, and physics-based simplified models. Plain space mapping can fail (intuition

often fails); remedies are widely discussed in the literature [20], [30], [107], [114].

Aggressive space mapping (ASM) arose from Madsen's realization that optimizing a fine model using a corresponding coarse model as a valid quasi-global approximation is equivalent to solving a system of nonlinear equations, and nonlinear equations benefit from Broyden's method of updating the Jacobian [14], [15], [107]. In essence, the ASM algorithm iteratively finds a solution to the following system of nonlinear equations:

$$f(x_f) = \mathbf{0} = p(x_f) - x_c^* \quad (3)$$

where $P(x_f)$ represents the mapping from the fine model design space x_f to the coarse model design space x_c , and x_c^* is the optimal coarse model design. Any root of the above system of equations, x_f^{SM} , also known as a space mapped solution, implies that the fine model response $R_f(x_f^{SM})$ sufficiently matches the coarse model optimal response $R_c(x_c^*)$, which is used as the target response [107]. The potential multiplicity in space-mapped solutions [107] as well as in coarse model local alignments is addressed in [20], [115], [116].

C. SPACE MAPPING GETS EXPANDED

Not surprisingly, in the early years following his announcement of space mapping, Bandler was frequently asked why his group seemed to be the only group applying space mapping. That trend was soon to revert by an explosion of diverse users with space mapping variations going well beyond the initial aggressive space mapping formulation.

In 1998, Q. J. Zhang from Carleton University, long-time collaborator and early contributor to the success of OSA's harmonic balance simulation and parameter extraction capabilities, visited McMaster University to deliver some seminars on his emerging work on artificial neural networks for modeling microwave components [117], [118], [119], [120]. Previously intrigued by the relationship between the coarse and fine models in space mapping, Rayas-Sánchez was inspired by that visit to pioneer the productive connection between space mapping and artificial neural networks for RF and microwave modeling and design optimization [23], [27], [121], [122], [123]. Fruitful collaboration with Q. J. Zhang's group at Carleton University was key for the wide development of neural space mapping approaches, well-beyond Bandler's research group [29], [124], [125], [126], [127], [128], [129], [130], [131], [132], [133], [134], [135].

Another key event that contributed to the wide spread of space mapping was the workshop entitled "Microwave Component Design Using Space Mapping Methodologies," organized by Bandler as part of the 2002 IEEE MTT-S International Microwave Symposium (IMS), in Seattle, WA. Vicente Boria, from the Polytechnical University of Valencia, Spain, an attendee of that workshop, immediately found innovative applications of space mapping to waveguide filter design [136], [137], [138], established a fruitful collaboration with Rayas-Sánchez [139], including reciprocal visits in 2006 in Guadalajara, Mexico (see Fig. 15), and Valencia, Spain,



FIGURE 15. Vicente Boria (right) visiting ITESO – The Jesuit University of Guadalajara, Mexico, in 2006. Luis Roglá (center), Ph.D. candidate from the Polytechnical University of Valencia, Spain, during internship at ITESO. José Rayas-Sánchez (left).

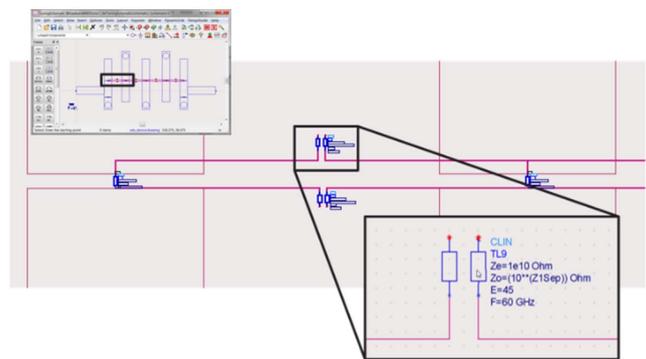


FIGURE 16. Tuning ports: Example using Agilent ADS to tune coupling between resonators. Most of the filter is EM analyzed in Sonnet. Tuning is done in ADS [159]. Courtesy of Jim Rautio.

and later contributed to an even wider dissemination of space mapping [140], [141], [142], [143], [144], [145], [146].

Logical enhancements of space mapping emerged organically [16]. Among the many variations on space mapping formulations [20], [31], [147], the three most closely related to common engineering practice are the aggressive, implicit, and tuning space mapping algorithms. The first one is by far the most popular [31], [107]. Implicit space mapping [148], [149], [150], [151], [152], [188] naturally exploits coarse model preassigned parameters not used as optimization variables to iteratively enhance the mapped coarse model whose direct optimization yields the next iterate. Tuning space mapping [153], [154], [155] exploits tuning ports, tunable lumped elements, and fixed electromagnetic models to construct fast tuning models for direct optimization with electromagnetic accuracy (see Fig. 16)—a smart engineering approach to filter design [156], [157], [158], [159], [160]. This approach was foreshadowed in 1997 [115].



FIGURE 17. Significant contributors to the evolution of space mapping at the IMS-2017 in Honolulu. From left to right: Bandler, Yu, Boria, Nikolova, Rayas-Sánchez, Zhang, and Biernacki.

Output space mapping [162], [163] follows a more mathematically rather than engineering inspired approach [164], [165], [166], [167] by directly correcting the coarse model responses for a better match to those of the fine model.

Synthesis or inverse modeling space mapping approaches have also been successfully employed for RF and microwave design. Here, an inverse mapping (from the coarse model to the fine model design space) is developed, either by a neural [168], [169] or linear [170], [171], [172], [173], [174] mapping, allowing the prediction of the next iterate by simply evaluating the current inverse mapping at the optimal coarse model design.

Even though space mapping emerged as an efficient solution for optimizing high-fidelity models, it was soon extended to develop space-mapped models for accurate and inexpensive statistical analysis and yield optimization [28,] [171], [175], [176], [178], [179], [180], [181].

Fig. 17 shows a photo of some of the main contributors to the utilization and expansion of space mapping.

D. SPACE MAPPING IS APPLIED BEYOND RF AND MICROWAVES

Although it started in the microwave engineering arena, space mapping optimization has found applications in a plethora of diverse disciplines. For instance, simple linear input as well as aggressive space mapping have been applied in areas such as materials design, environmental sciences, medical instrumentation, magnetic circuits, electric motors, chemical, civil, mechanical, biomedical, aerodynamic, aeronautical, and aerospace engineering, for example, [107], [108], [109], [110].

E. MEASUREMENT-BASED SPACE MAPPING

Diverse commercially available EDA systems have been used to implement fine models of the optimized structures, and more recently, measurement-based physical platforms have also been exploited as “fine models”, both in classical microwave design challenges [182], [183], [184], [185], [186], [187], [188], [189], [190], [191], [192], as well as in other

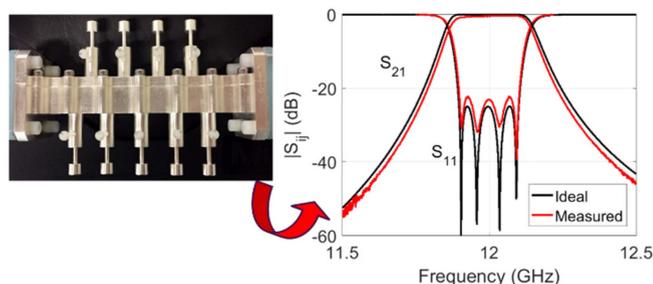


FIGURE 18. Space mapping applied to physically tune, in a few “iterations,” a 4-pole inductively coupled rectangular waveguide filter with tuning screws in cavities and coupling windows [186]. Courtesy of Vicente Boria and Marco Guglielmi.

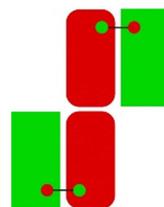


FIGURE 19. The SMSMEO logo designed by Bandler, inspired by waveguide filter design [201]. Coarse models are represented by the rectangles with right-angle (ideal) corners, while fine models are represented by the rectangles with the rounded (manufactured) corners. Complementary links between them represent the mappings. The set forms an “S” (space).

related fields, such as high-speed digital design [193], [194], [195], [196], opening new challenges and opportunities [197]. Fig. 18 illustrates an example of a waveguide filter physically tuned by space mapping in a few “iterations” [186].

F. SPACE MAPPING MEETS SURROGATE MODELING

A key historical meeting organized by Madsen and Bandler brought together the mathematical optimization community with the engineering community working on microwave design optimization. This was the first *International Workshop on Surrogate Modeling and Space Mapping for Engineering Optimization (SMSMEO)*, celebrated in 2000 at the Technical University of Denmark, in Lyngby, outside Copenhagen [198]. The second and third edition of this workshop took place in 2006 in Lyngby, Denmark [199], and 2012 in Reykjavik, Iceland [200], respectively. These events established a fruitful dialog bridge between the mathematics and engineering communities interested in optimizing computationally expensive functions.

Indeed, the first SMSMEO (see Fig. 19) in 2000 brought Bandler’s and Madsen’s groups (space mapping) into discussion with that of mathematician John Dennis of Rice University (surrogate modeling). Notably, Madsen initially hesitated in using “space mapping” as part of the name of the *International Workshop on Surrogate Modelling and Space Mapping*. He anticipated controversy, and he was right. The debate in Lyngby proved lively. The bottom line is that mathematicians then used Taylor-based (linear and/or



FIGURE 20. Slawomir Koziel (right) and John Bandler (left) at the IMS-2018 in Philadelphia, PA.

quadratic) nonphysical localized coarse models as their underlying “surrogates” while space-mapping-oriented engineers underpinned their algorithms with quasi-global physics-based models based on engineering knowledge.

Direct electromagnetic optimization algorithms using abstract mathematical models is dramatically at odds with what a skilled engineer can achieve via an experience-tested “feel” for the problem. In hindsight, the effectiveness of space mapping utilizing relevant physics-based surrogates should not have been surprising.

VI. SURROGATE METHODOLOGIES AND BAYESIAN APPROACHES

Slawek Koziel (see Fig. 20) was with Bandler’s Simulation Optimization Systems Research Laboratory at McMaster University from 2004 to 2007. He applied his background in mathematics, making ground-breaking advances in the effective use of mathematically-based surrogate-based optimization techniques [21], [202], [203], as well as in the theoretical foundations of space mapping [204], [205], [206], [207], [208]. Now with Reykjavik University, Iceland, he is surely the most identifiable researcher in our community with respect to all aspects of surrogate methodologies and their applications. Koziel and his team spearheaded innovative electromagnetic simulation-driven and surrogate-based optimization procedures for microwave circuits and antenna design, including variable-fidelity optimization frameworks [209], [210], [211]; surrogate-assisted tuning [191], [212], [213], yield estimation [214], [215], and multi-objective optimization [216], [217], [218], [219], [220]; methodologies for rapid re-design by inverse surrogates [221], [222]; microwave component miniaturization [223], [224]; dimensionally reduced and domain confined surrogates [225], [226], [227]; response feature-based nominal design [228], [229], [230], [231], [232], yield optimization [233], [234], [235], [236], [237], and robust design by tolerance maximization [238], [239], [240].

Bayesian approaches [241] are suitable for dealing with computationally expensive models, especially those with noise-corrupted responses, including high-fidelity models based on physical measurements subject to statistical uncertainty and varying operating or environmental conditions [31].

Bayesian optimization has been successfully applied to the design of microwave power amplifiers [242], [243], signal integrity and high-speed channels [244], [245], [246], [247], RF high-dimensional global optimization [248], parallel electromagnetic optimization of microstrip filters [249], and RF circuit synthesis via ANN-enhanced Gaussian process [250], among other areas.

VII. COGNITION-DRIVEN DESIGN

There are clear parallels between space mapping and human cognition (the engineer’s traditional but mysterious “feel” for a problem): (1) the simple, intuitive examples of space mapping like the cheese-cutting problem [20]; (2) the popularity of aggressive space mapping as shown by Rayas-Sánchez, (3) the parallels with space mapping found in Kahneman [251]: his System 1, fast and intuitive, and his System 2 that is slow and effortful; (4) advances in space-mapping-based design using cognition-style markers like response features [233], [252].

Bandler created several simple illustrations of the space mapping concept in action [20]. They started with the so-called “cheese-cutting” illustration, then came the “wedge problem,” “cake-cutting,” “shoe-selection,” [16] “parachute landing game” [5], [253], and other illustrations or games. These manifestations of aggressive space mapping date back to Bandler’s shock the night before he was scheduled to address mathematician and collaborator Kaj Madsen’s undergraduate optimization class at the Technical University of Denmark. Madsen abruptly questioned his own understanding of space mapping and indeed the concept of “model” as understood by engineers. Bandler scrambled overnight to find a common sense illustration suitable for engineer and mathematician alike and came up with the cheese-cutting illustration in time for Madsen’s class the next day.

Similarly, Rayas-Sánchez was confused when Bandler presented him privately with the “parachute landing game” [253] during the IMS-2015 in Phoenix. What did this one-dimensional task have to do with microwave design optimization? It was not until Rayas-Sánchez tried out the game on Bandler’s laptop that he identified the relationship (calibration) between the inherent “fine model” and his own evolving mental “coarse model” of the game. There appeared to be three overlapping phases to the task required, (1) a familiarization phase (building knowledge and confirming a mental model), (2) a refinement phase (learning to trust the mental model while replaying the game on each reset), and (3) a mapping update phase (realizing that a “best” guess at the next iterate requires information from more than the immediately preceding iterate).

Bandler writes [16], “Aggressive space mapping efficiently invokes inner loops of conventional optimization—common sense at work—often yielding excellent results in a few iterations. The aggressive space mapping update/execution process is itself optimization on a higher level—meta-optimization?—a process that uncannily mimics both common sense and the expert’s ‘feel’.”

Needless to say, Madsen was shocked, as many people within and outside our community still are, that space mapping can be reduced to common sense, like not too hot, not too cold, just right.

If we consider cognition-driven design methodologies—along with machine learning and artificial intelligence—in terms of common sense, human experience, space mapping, etc., we might look for new avenues of mutual collaboration and dialogue with psychologists, neuroscientists, game developers, linguists, playwrights, etc., and work with human subjects and explore the emotional element. See, for example, [254].

VIII. CONCLUSION

The year 2023 marks 30 elapsed years since 1993 that engineering optimization, particularly microwave electromagnetic optimization, has exploited space mapping technology and related surrogate methodologies. It took 30 years of research in microwaves, computer-aided design, and optimization of circuits and systems for Bandler to reach that half-way milestone, 30 years during which the engineer's mysterious feel proved elusive, decades populated by visionaries and champions of numerical and iterative techniques to design optimization, and some inevitable skeptics, industrial and academic, many of whom would eventually be won over.

We touched on circuit-oriented optimization, adjoint sensitivities, yield-driven design, nonlinear circuit design, and gradient-based optimization algorithms such as Madsen's minimax and related formulations that have stood the test of time. We emphasized the origins and subsequent evolution of microwave electromagnetic optimization.

We devoted much attention to what we believe is a decade of acceptance, final acceptance by academia and industry, of iterative numerical optimization tools and corresponding commercial software offerings. We provided a timeline of major commercial optimization-oriented solutions and events, and noted the impact of the MIMIC Program.

We outlined the backstory to space mapping: reactions to it by the scientific and engineering communities, some success stories, some failures, and its ongoing reinvention as "cognition-driven design." We suggested the essence of space mapping [255], the expansion of space mapping into variations such as implicit and tuning space mapping, applications beyond RF and microwaves, measurement-based space mapping, and a synergistic period when engineers and mathematicians met to discuss space mapping vis-à-vis surrogate modeling. Finally, we touched on ever expanding surrogate methodologies.

A brief section sketched what Bandler has termed "cognition-driven design." We indicated its roots in space mapping, named simple illustrations and suggested a link of the concept to common sense.

We indicate but do not elaborate in our paper on the current state of the art of advanced surrogate technologies and their myriad applications, nor elaborate on our vision of the future.

Such details, we believe, are dealt with by Rayas-Sánchez et al. [31]. Instead, we provided a selected backstory and the likely early cornerstones of optimization technology in microwaves and RF.

In Bandler's words [5]: "Finally, the engineer's mysterious "feel" will most likely be automated, with accurate predictions of successes rather than explanations of failures being the ultimate goal."

Returning to the first paragraph in our introduction: "Fully automated design and optimization is surely one of the ultimate goals of computer-aided design." After more than five decades, it remains one of our ultimate goals: we still have much to do!

ACKNOWLEDGMENT

The authors thank Melissa Skudra for her skillful assistance in researching, scanning, and archiving reports, manuals, and other documents.

Countless colleagues have contributed to our innovations in design automation in microwave engineering and electromagnetic optimization over the years or have championed our technical work.

Fellow Ph.D. student at Imperial College London, Al Wexler offered Bandler a two-year postdoctoral fellowship at the University of Manitoba, Canada, in Bandler's formative years in the 1960s, with total freedom in his research, and technical and programming support. Bill Getsinger invited Wexler and Bandler to join him in the foundation of MTT-1 in 1968. Getsinger flew to Winnipeg from Boston in 1968 to invite each of them to contribute an invited paper for the benchmark *IEEE Transactions on Microwave Theory and Techniques Special Issue on Computer-Oriented Microwave Practices*, August 1969. Again, Bill Getsinger offered Bandler and Wexler wide latitude in their manuscripts.

A partial list of key colleagues and/or co-authors over the decades, in alphabetical order, includes H.L. Abdel Malek, Reza Amineh, Mohamed H. Bakr, Radoslaw (Radek) Biernacki, Kristen Booth, Qian Cai, Christakis (Chris) Charalambous, José L. Chávez-Hurtado, S.H. (Steve) Chen, James H.K. Chen, Q.S. (Shasha) Cheng, Shahrokh Daijavad, Sameh Dakroury, M.A. El-Kady, Feng Feng, P.A. (Peter) Grobelny, Vladimir Gutiérrez-Ayala, Mohamed A. Ismail, Witold (Witek) Kellermann, Erin Kiley, Slawomir (Slawek) Koziel, P.C. (Peter) Liu, Patrick A. Macdonald, Ahmed S. Mohamed, Natalia Nikolova, Kaj Madsen, Dzevat Omeragić, Frank Pedersen, Steve Porter, Francisco Rangel-Patiño, Monique Renault, M.R.M. Rizk, Luis Roglá, A.E. Salama, Rudolph Seviora, Jacob Søndergaard, Jian Song, Janusz Starzyk, Daniel Tajik, Salvador Talisa, Joan Tripp, Herman Tromp, Shen Ye, Q.J. Zhang, Jiang Zhu, Wlodek Zuberek.

Key champions, in alphabetical order, include Vicente Boria, Maurizio Bozzi, Christophe Caloz, Robert Caverly, Walter Curtice, William Getsinger, Marco Guglielmi, K.C. Gupta, Luis Hendrick, Robert C. Hansen, Wolfgang Hofer, Rolf Jansen, Peter Johns, Jack Kirkaldy, Chandra Kudisia, Raafat

Mansour, Michel Nakhla, Natalia Nikolova, Robert Pucel, James C. (Jim) Rautio, Vittorio Rizzoli, Fred Rosenbaum, Naresh K. Sinha, Jan Snel, Sir Christopher Snowden, Roberto Sorrentino, Dan Swanson, Jr., Edward Della Torre, Robert Trew, John Vlachopoulos, Al Wexler, Dave Wilson, Ingo Wolff, Ke Wu, and Ming Yu.

The additional contributions and support of Greg Atkinson, Beth Bandler, Stephen Elop, Farzad Foroutan, Cheryl Gies, Denys Shumakov, and Peter Thoma, are gratefully acknowledged.

REFERENCES

- [1] G. C. Temes and D. A. Calahan, "Computer-aided network optimization the state-of-the-art," *Proc. IEEE*, vol. 55, no. 11, pp. 1832–1863, Nov. 1967.
- [2] S. Director, "Survey of circuit-oriented optimization techniques," *IEEE Trans. Circuit Theory*, vol. 18, no. 1, pp. 3–10, Jan. 1971.
- [3] W. J. Getsinger, "Special issue on computer-oriented microwave practices," *IEEE Trans. Microw. Theory Techn.*, vol. 17, no. 8, p. 415, Aug. 1969.
- [4] J. W. Bandler, "Optimization methods for computer-aided design," *IEEE Trans. Microw. Theory Techn.*, vol. 17, no. 8, pp. 533–552, Aug. 1969.
- [5] J. W. Bandler, "The journey to automated design optimization and a vision for the future," in *Proc. IEEE MTT-S Int. Microw. Symp.*, 2017, pp. 1517–1519.
- [6] I. A. Cermak, W. J. Getsinger, B. W. Leake, A. S. Vander Vorst, and D. Varon, "The status of computer-oriented microwave practices (panel discussion)," *IEEE Trans. Microw. Theory Techn.*, vol. 22, no. 3, pp. 155–160, Mar. 1974.
- [7] K. Madsen, "An algorithm for minimax solution of overdetermined systems of non-linear equations," *J. App. Math. (Inst. Math. Appl.)*, vol. 16, pp. 321–328, 1975.
- [8] J. Bandler, P. C. Liu, and H. Tromp, "A nonlinear programming approach to optimal design centering, tolerancing and tuning," *IEEE Trans. Circuits Syst.*, vol. 23, no. 3, pp. 155–165, Mar. 1976.
- [9] R. K. Brayton, G. D. Hachtel, and A. L. Sangiovanni-Vincentelli, "A survey of optimization techniques for integrated-circuit design," *Proc. IEEE*, vol. 69, no. 10, pp. 1334–1362, Oct. 1981.
- [10] J. W. Bandler, P. C. Liu, and J. H. K. Chen, "Worst case network tolerance optimization," *IEEE Trans. Microw. Theory Techn.*, vol. 23, no. 8, pp. 630–641, Aug. 1975.
- [11] J. W. Bandler and Q. J. Zhang, "An automatic decomposition approach to optimization of large microwave systems," *IEEE Trans. Microw. Theory Techn.*, vol. 35, no. 12, pp. 1231–1239, Dec. 1987.
- [12] J. W. Bandler, S. Ye, R. M. Biernacki, S. H. Chen, and D. G. Swanson, Jr., "Minimax microstrip filter design using direct EM field simulation," in *Proc. IEEE MTT-S Int. Microw. Symp.*, 1993, pp. 889–892.
- [13] J. W. Bandler, R. M. Biernacki, S. H. Chen, P. A. Grobelny, and R. H. Hemmers, "Space mapping technique for electromagnetic optimization," *IEEE Trans. Microw. Theory Techn.*, vol. 42, no. 12, pp. 2536–2544, Dec. 1994.
- [14] J. W. Bandler, R. M. Biernacki, S. H. Chen, R. H. Hemmers, and K. Madsen, "Aggressive space mapping for electromagnetic design," in *Proc. IEEE MTT-S Int. Microw. Symp.*, 1995, vol. 3, pp. 1455–1458.
- [15] J. W. Bandler, R. M. Biernacki, S. H. Chen, R. H. Hemmers, and K. Madsen, "Electromagnetic optimization exploiting aggressive space mapping," *IEEE Trans. Microw. Theory Techn.*, vol. 43, no. 12, pp. 2874–2882, Dec. 1995.
- [16] J. W. Bandler, "Have you ever wondered about the engineer's mysterious 'feel' for a problem?," *IEEE Can. Rev.*, no. 70, pp. 50–60, 2013.
- [17] A. J. Booker, J. E. Dennis, Jr., P. D. Frank, D. B. Serafini, V. Torczon, and M. W. Trosset, "A rigorous framework for optimization of expensive functions by surrogates," *Struct. Optim.*, vol. 17, no. 1, pp. 1–13, Feb. 1999.
- [18] A. H. Zaabab, Q. J. Zhang, and M. Nakhla, "A neural network modeling approach to circuit optimization and statistical design," *IEEE Trans. Microw. Theory Techn.*, vol. 43, no. 6, pp. 1349–1358, Jun. 1995.
- [19] A. Veluswami, M. S. Nakhla, and Q. J. Zhang, "The application of neural networks to EM-based simulation and optimization of interconnects in high-speed VLSI circuits," *IEEE Trans. Microw. Theory Techn.*, vol. 45, no. 5, pp. 712–723, May 1997.
- [20] J. W. Bandler et al., "Space mapping: The state of the art," *IEEE Trans. Microw. Theory Techn.*, vol. 52, no. 1, pp. 337–361, Jan. 2004.
- [21] M. B. Yelten, T. Zhu, S. Koziel, P. D. Franzon, and M. B. Steer, "Demystifying surrogate modeling for circuits and systems," *IEEE Circuits Syst. Mag.*, vol. 12, no. 1, pp. 45–63, Jan.–Mar. 2012.
- [22] Q. J. Zhang and K. C. Gupta, *Neural Networks for RF and Microwave Design*. Norwood, MA, USA: Artech House, 2000.
- [23] J. E. Rayas-Sánchez, "EM-based optimization of microwave circuits using artificial neural networks: The state of the art," *IEEE Trans. Microw. Theory Techn.*, vol. 52, no. 1, pp. 420–435, Jan. 2004.
- [24] M. H. Bakr, J. W. Bandler, K. Madsen, J. E. Rayas-Sánchez, and J. Søndergaard, "Space mapping optimization of microwave circuits exploiting surrogate models," *IEEE Trans. Microw. Theory Techn.*, vol. 48, no. 12, pp. 2297–2306, Dec. 2000.
- [25] J. W. Bandler, D. M. Hailu, K. Madsen, and F. Pedersen, "A space-mapping interpolating surrogate algorithm for highly optimized EM-based design of microwave devices," *IEEE Trans. Microw. Theory Techn.*, vol. 52, no. 11, pp. 2593–2600, Nov. 2004.
- [26] S. Koziel and J. W. Bandler, "Space-mapping optimization with adaptive surrogate model," *IEEE Trans. Microw. Theory Techn.*, vol. 55, no. 3, pp. 541–547, Mar. 2007.
- [27] M. H. Bakr, J. W. Bandler, M. A. Ismail, J. E. Rayas-Sánchez, and Q. J. Zhang, "Neural space mapping optimization for EM-based design," *IEEE Trans. Microw. Theory Techn.*, vol. 48, no. 12, pp. 2307–2315, Dec. 2000.
- [28] J. E. Rayas-Sánchez and V. Gutiérrez-Ayala, "EM-based Monte Carlo analysis and yield prediction of microwave circuits using linear-input neural-output space mapping," *IEEE Trans. Microw. Theory Techn.*, vol. 54, no. 12, pp. 4528–4537, Dec. 2006.
- [29] V. Gutiérrez-Ayala and J. E. Rayas-Sánchez, "Neural input space mapping optimization based on nonlinear two-layer perceptrons with optimized nonlinearity," *Int. J. RF Microw. CAE*, vol. 20, no. 5, pp. 512–526, Sep. 2010.
- [30] M. B. Steer, J. W. Bandler, and C. M. Snowden, "Computer aided design of RF and microwave circuits and systems," *IEEE Trans. Microw. Theory Techn.*, vol. 50, no. 3, pp. 996–1005, Mar. 2002.
- [31] J. E. Rayas-Sánchez, S. Koziel, and J. W. Bandler, "Advanced RF and microwave design optimization: A journey and a vision of future trends," *IEEE J. Microwaves*, vol. 1, no. 1, pp. 481–493, Jan. 2021.
- [32] G. P. Gibiino et al., "TC-2 design automation committee—On the future of RF and microwave design automation—2022," *IEEE Microw. Mag.*, vol. 23, no. 11, pp. 104–105, Nov. 2022.
- [33] Wikipedia. "Optimization Systems Associates," [Online]. Available: https://en.wikipedia.org/wiki/Optimization_Systems_Associates
- [34] J. A. Nelder and R. Mead, "A simplex method for function minimization," *Comput. J.*, vol. 7, no. 4, pp. 308–313, Jan. 1965.
- [35] J. W. Bandler and P. A. Macdonald, "Optimization of microwave networks by razor search," *IEEE Trans. Microw. Theory Techn.*, vol. 17, no. 8, pp. 552–562, Aug. 1969.
- [36] S. Director and R. Rohrer, "The generalized adjoint network and network sensitivities," *IEEE Trans. Circuit Theory*, vol. 16, no. 8, pp. 318–323, Aug. 1969.
- [37] J. W. Bandler and R. E. Seviara, "Current trends in network optimization," *IEEE Trans. Microw. Theory Techn.*, vol. 18, no. 12, pp. 1159–1170, Dec. 1970.
- [38] J. W. Bandler and R. E. Seviara, "Wave sensitivities of networks," *IEEE Trans. Microw. Theory Techn.*, vol. 20, no. 2, pp. 138–147, Feb. 1972.
- [39] J. Hald and K. Madsen, "Combined LP and quasi-Newton methods for minimax optimization," *Math. Prog.*, vol. 20, no. 1, pp. 49–62, 1981.
- [40] J. W. Bandler and S. H. Chen, "Circuit optimization: The state of the art," *IEEE Trans. Microw. Theory Techn.*, vol. 36, no. 2, pp. 424–443, Feb. 1988.
- [41] J. W. Bandler, "Guest editorial," *IEEE Trans. Microw. Theory Techn.*, vol. 45, no. 5, pp. 709–711, May 1997.
- [42] J. W. Bandler and C. Charalambous, "Practical least pth optimization of networks," *IEEE Trans. Microw. Theory Techn.*, vol. 20, no. 12, pp. 834–840, Dec. 1972.

- [43] B. J. Karafin, "The optimum assignment of component tolerances for electrical networks," *Bell Syst. Tech. J.*, vol. 50, pp. 1225–1242, 1971.
- [44] J. F. Pinel and K. A. Roberts, "Tolerance assignment in linear networks using nonlinear programming," *IEEE Trans. Circuit Theory*, vol. 19, no. 5, pp. 475–479, Sep. 1972.
- [45] J. W. Bandler, P. C. Liu, and H. Tromp, "Integrated approach to microwave design," *IEEE Trans. Microw. Theory Techn.*, vol. 24, no. 9, pp. 584–591, Sep. 1976.
- [46] J. W. Bandler, S. H. Chen, and S. Daijavad, "Microwave device modeling using efficient l_1 optimization: A novel approach," *IEEE Trans. Microw. Theory Techn.*, vol. 34, no. 12, pp. 1282–1293, Dec. 1986.
- [47] J. W. Bandler, S. H. Chen, R. M. Biernacki, L. Gao, K. Madsen, and H. Yu, "Huber optimization of circuits: A robust approach," *IEEE Trans. Microw. Theory Techn.*, vol. 41, no. 12, pp. 2279–2287, Dec. 1993.
- [48] *OSA90/hope™ Version 3.5 Technical Brief*, Dundas, ON, Canada: Optim. Syst. Assoc., 1996. [Online]. Available: <https://web.archive.org/web/19961113060558/http://osacad.com:80/osa90.htm>.
- [49] G. Li and K. Madsen, "Robust nonlinear data fitting," in *Numerical Analysis* (Pitman Research Notes in Mathematics Series 170), D. F. Griffiths and G. A. Watson, Eds. White Plains, New York, NY, USA: Longman, 1988, pp. 176–191.
- [50] J. W. Bandler, S. H. Chen, R. M. Biernacki, and K. Madsen, "The Huber concept in device modeling, circuit diagnosis and design centering," in *Proc. IEEE Int. Symp. Circuits Syst.*, 1994, pp. 129–132.
- [51] K. Madsen, O. Nielson, H. Schjaer-Jacobsen, and L. Thrane, "Efficient minimax design of networks without using derivatives," *IEEE Trans. Microw. Theory Techn.*, vol. 23, no. 10, pp. 803–809, Oct. 1975.
- [52] K. Madsen, H. Schjaer-Jacobsen, and J. Voldby, "Automated minimax design of networks," *IEEE Trans. Circuits Syst.*, vol. 22, no. 10, pp. 791–795, Oct. 1975.
- [53] K. Madsen and H. Schjaer-Jacobsen, "Singularities in minimax optimization of networks," *IEEE Trans. Circuits Syst.*, vol. 23, no. 7, pp. 456–460, Jul. 1976.
- [54] J. W. Bandler, W. Kellermann, and K. Madsen, "A superlinearly convergent minimax algorithm for microwave circuit design," *IEEE Trans. Microw. Theory Techn.*, vol. 33, no. 12, pp. 1519–1530, Dec. 1985.
- [55] J. W. Bandler, S. H. Chen, S. Daijavad, and K. Madsen, "Efficient optimization with integrated gradient approximations," *IEEE Trans. Microw. Theory Techn.*, vol. 36, no. 2, pp. 444–455, Feb. 1988.
- [56] V. Rizzoli and A. Neri, "State of the art and present trends in nonlinear microwave CAD techniques," *IEEE Trans. Microw. Theory Techn.*, vol. 36, no. 2, pp. 343–365, Feb. 1988.
- [57] J. W. Bandler, Q. J. Zhang, and R. M. Biernacki, "A unified theory for frequency-domain simulation and sensitivity analysis of linear and nonlinear circuits," *IEEE Trans. Microw. Theory Techn.*, vol. 36, no. 12, pp. 1661–1669, Dec. 1988.
- [58] *Microwave Harmonica User's Manual*, Paterson, NJ, USA: Compact Softw. Inc., 1987.
- [59] *OSA90*, Dundas, ON, Canada: Optim. Syst. Assoc. Inc., 1990.
- [60] J. W. Bandler, Q. J. Zhang, J. Song, and R. M. Biernacki, "FAST gradient based yield optimization of nonlinear circuits," *IEEE Trans. Microw. Theory Techn.*, vol. 38, no. 11, pp. 1701–1710, Nov. 1990.
- [61] J. W. Bandler, R. M. Biernacki, Q. Cai, S. H. Chen, S. Ye, and Q. J. Zhang, "Integrated physics-oriented statistical modeling, simulation and optimization," *IEEE Trans. Microw. Theory Techn.*, vol. 40, no. 7, pp. 1374–1400, Jul. 1992.
- [62] *RomPE™ User's Manual Version 1.2*, Dundas, ON, Canada: Optim. Syst. Assoc. Inc., 1989. [Online]. Available: <https://macsphere.mcmaster.ca/handle/11375/27782>.
- [63] *HarPE™ Version 2.0 Technical Brief*, Dundas, ON, Canada: Optim. Syst. Assoc. Inc., 1996. [Online]. Available: <https://web.archive.org/web/19961113055901/http://osacad.com/harpe.htm>.
- [64] *HP HFSS*, Santa Rosa, CA, USA: Hewlett-Packard EEsof Division, 1991.
- [65] *Maxwell Eminence*, Pittsburgh, PA, USA: Ansoft Corp., 1991.
- [66] J. W. Bandler, R. M. Biernacki, S. H. Chen, D. G. Swanson, and S. Ye, "Microstrip filter design using direct EM field simulation," *IEEE Trans. Microw. Theory Techn.*, vol. 42, no. 7, pp. 1353–1359, Jul. 1994.
- [67] *Empipe™ Version 3.5 Technical Brief*, Dundas, ON, Canada: Optim. Syst. Assoc. Inc., 1996. [Online]. Available: <https://web.archive.org/web/19961113055752/http://osacad.com:80/empipe.htm>.
- [68] *OSA90/hope™ Version 4.0 User's Manual*, Dundas, ON, Canada: Optim. Syst. Assoc. Inc., 1997. [Online]. Available: <https://macsphere.mcmaster.ca/handle/11375/27835>.
- [69] *Empipe™ Version 4.0 User's Manual*, Dundas, ON, Canada: Optim. Syst. Assoc. Inc., 1997. [Online]. Available: <https://macsphere.mcmaster.ca/handle/11375/27790>.
- [70] *em™ User's Manual*, Liverpool, New York, NY, USA: Sonnet Softw., Inc., May 1992.
- [71] J. W. Bandler, R. M. Biernacki, S. H. Chen, P. A. Grobelny, and S. Ye, "Yield-driven electromagnetic optimization via multilevel dimensional models," *IEEE Trans. Microw. Theory Techn.*, vol. 41, no. 12, pp. 2269–2278, Dec. 1993.
- [72] P. P. M. So, W. J. R. Hoefler, J. W. Bandler, R. M. Biernacki, and S. H. Chen, "Hybrid frequency/time domain field theory based CAD of microwave circuits," in *Proc. 23rd Eur. Microw. Conf.*, 1993, pp. 218–219.
- [73] P. P. M. So and W. J. R. Hoefler, "3D-TLM techniques for field-based optimization of electromagnetic structures," in *Proc. IEEE APS Int. Symp. URSI Nat. Radio Sci. Meeting*, 1994, vol. 2, pp. 1116–1119.
- [74] *Empipe3D™ Version 3.5 Technical Brief*, Dundas, ON, Canada: Optim. Syst. Assoc. Inc., 1996. [Online]. Available: <https://web.archive.org/web/19961113055553/http://osacad.com:80/emp3d.htm>.
- [75] *Empipe3D™ Version 4.0 User's Manual*, Dundas, ON, Canada: Optim. Syst. Assoc. Inc., 1997. [Online]. Available: <https://macsphere.mcmaster.ca/handle/11375/27893>.
- [76] *High Frequency Structure Simulator (HFSS)*, Santa Rosa, CA, USA: Hewlett-Packard Corp., EEsof Division, 1996.
- [77] *Maxwell Eminence*, Pittsburgh, PA, USA: Ansoft Corp., 1996.
- [78] J. W. Bandler, R. M. Biernacki, S. H. Chen, L. W. Hendrick, and D. Omeragić, "Electromagnetic optimization of 3-D structures," *IEEE Trans. Microw. Theory Techn.*, vol. 45, no. 5, pp. 770–779, May 1997.
- [79] A. Kozak and W. K. Gwarek, "Unrestricted arbitrary shape optimization based on 3D electromagnetic simulation," in *Proc. IEEE MTT-S Int. Microw. Symp.*, 1998, vol. 1, pp. 17–20.
- [80] N. K. Nikolova, J. Zhu, D. Li, M. H. Bakr, and J. W. Bandler, "Sensitivity analysis of network parameters with electromagnetic frequency-domain simulators," *IEEE Trans. Microw. Theory Techn.*, vol. 54, no. 2, pp. 670–681, Feb. 2006.
- [81] P. Thoma, "A framework for implementing various techniques for EM based optimization," in *Proc. IEEE MTT-S Int. Microw. Symp. Workshop Simul.- Surrogate-Driven Microw. Des. Technol.*, 2011.
- [82] *CST Microwave Studio Version 2013*, CST AG, Darmstadt, Germany, 2013.
- [83] *Ansys HFSS*, ANSYS, Inc., Southpointe, 2600 Ansys Drive, Canonsburg, PA, USA.
- [84] *SIMULIA CST Studio Suite*, Dassault Systèmes Velizy Villacoublay Cedex, France.
- [85] N. K. Nikolova and R. Kazemivala, "Analytical expressions for field-based response sensitivity analysis and their application in microwave design and imaging," in *Proc. IEEE MTT-S Int. Microw. Symp.*, 2022, pp. 104–106.
- [86] L. Balewski et al., "Step on it: Bringing fullwave finite-element microwave filter design up to speed," *IEEE Microw. Mag.*, vol. 21, no. 3, pp. 34–49, Mar. 2020.
- [87] F. Feng, J. Zhang, J. Jin, W. Na, S. Yan, and Q. J. Zhang, "Efficient FEM-based EM optimization technique using combined Lagrangian method with Newton's method," *IEEE Trans. Microw. Theory Techn.*, vol. 68, no. 6, pp. 2194–2205, Jun. 2020.
- [88] X. Li, F. Feng, J. Zhang, W. Zhang, and Q. J. Zhang, "Advanced simulation-inserted optimization using combined Quasi-Newton method with Lagrangian method for EM-based design optimization," *IEEE Trans. Microw. Theory Techn.*, vol. 70, no. 8, pp. 3753–3764, Aug. 2022.
- [89] E. D. Cohen, "The MIMIC program-key to affordable MMICs for DoD systems," in *Proc. IEEE Microw. Millimeter-Wave Monolithic Circuit Symp.*, 1988, pp. 1–4.
- [90] E. D. Cohen, "MIMIC-An opportunity that must not be missed," in *Proc. Eur. Microw. Conf.*, 1989, pp. 3–12.
- [91] E. D. Cohen, "The MIMIC program: A retrospective," *IEEE Microw. Mag.*, vol. 13, no. 4, pp. 77–88, May/June 2012.
- [92] J. W. Bandler, "Press release: Optimization Systems Associates Inc. a key participant in Raytheon/Texas Instruments Joint Venture MIMIC Program," Sep. 1988. [Online]. Available: <https://macsphere.mcmaster.ca/handle/11375/27735>

- [93] Wikipedia. "Compact Software," [Online]. Available: https://en.wikipedia.org/wiki/Compact_Software
- [94] Wikipedia. "Path Wave Design," [Online]. Available: https://en.wikipedia.org/wiki/PathWave_Design
- [95] W. M. Zuberek, "SPICE-PAC, a package of subroutines for interactive simulation and optimization of circuits," in *Proc. IEEE Int. Conf. Comput. Des.*, 1984, pp. 492–496.
- [96] J. C. Rautio, "High frequency electromagnetic analysis—A historical perspective," in *Proc. IEEE MTT-S Int. Microw. Symp.*, 2008, pp. 715–718.
- [97] *EmpipeExpress™ Version 3.5 Technical Brief*, Optimization Systems Assoc. Inc., Dundas, ON, Canada, 1996. [Online]. Available: <https://web.archive.org/web/19961113055649/http://osacad.com/empex.htm>
- [98] *Empath User's Manual*, Release 4.0b, Sonnet Software, Inc., New York, NY, USA, May 1997.
- [99] "Microwave/Analog Tech," *Defense Advanced Research Projects Agency, 675 North Randolph Street, Arlington, VA 22203-2114*. [Online]. Available: <https://www.darpa.mil/about-us/timeline/microwave-analog-technology>
- [100] "Space mapping techniques for intelligent, automated, direct optimization-driven electromagnetic design of microwave and millimeter-wave circuits," in *OSA MAFET Proposal*, Dundas, ON, Canada: Optim. Syst. Assoc. Inc., 1995.
- [101] "HP acquires Optimization Systems Associates expands CAE software portfolio," Nov. 25, 1997. [Online]. Available: <https://web.archive.org/web/19980111022153/http://osacad.com/>
- [102] *Optimization Systems Associates Collection*, Hamilton, ON, Canada: McMaster Univ. [Online]. Available: <https://macsphere.mcmaster.ca/handle/11375/27423>.
- [103] "Non-linear CAD benchmark," *Microw. Eng. Europe*, pp. 11–15, Nov. 1993.
- [104] "CAD review: The 7GHz doubler circuit," *Microw. Eng. Europe*, pp. 43–53, May 1994.
- [105] J. W. Bandler, R. M. Biernacki, Q. Cai, S. H. Chen, and P. A. Grobely, "Integrated harmonic balance and electromagnetic optimization with Geometry Capture," in *Proc. IEEE MTT-S Int. Microw. Symp.*, 1995, pp. 793–796.
- [106] S. Koziel, Q. S. Cheng, and J. W. Bandler, "Space mapping," *IEEE Microw. Mag.*, vol. 9, no. 6, pp. 105–122, Dec. 2008.
- [107] J. E. Rayas-Sánchez, "Power in simplicity with ASM: Tracing the aggressive space mapping algorithm over two decades of development," *IEEE Microw. Mag.*, vol. 17, no. 4, pp. 64–76, Apr. 2016.
- [108] H. Gruebler, F. Krall, S. Leitner, and A. Muetze, "Space mapping-based fractional horsepower permanent magnet motor design," in *Proc. IEEE Int. Electric Machines Drives Conf.*, 2019, pp. 455–460.
- [109] K. Booth and J. Bandler, "Space mapping for codesigned magnetics: Optimization techniques for high-fidelity multidomain design specifications," *IEEE Power Electron. Mag.*, vol. 7, no. 2, pp. 47–52, Jun. 2020.
- [110] K. Booth, H. Subramanyan, J. Liu, and S. M. Lukic, "Parallel frameworks for robust optimization of medium-frequency transformers," *IEEE J. Emerg. Sel. Topics Power Electron.*, vol. 9, no. 4, pp. 5097–5112, Aug. 2021.
- [111] J. W. Bandler, R. M. Biernacki, S. H. Chen, P. A. Grobely, C. Moskowicz, and S. H. Talisa, "Electromagnetic design of high-temperature superconducting microwave filters," in *Proc. IEEE MTT-S Int. Microw. Symp.*, 1994, pp. 993–996.
- [112] J. E. Rayas-Sánchez and J. W. Bandler, "Basic space mapping: A retrospective and its application to design optimization of nonlinear RF and microwave circuits," in *Proc. Eur. Microw. Conf.*, 2022, pp. 12–15.
- [113] R. Fletcher and M. J. D. Powell, "A rapidly convergent descent method for minimization," *Comput. J.*, vol. 6, no. 2, pp. 163–168, Aug. 1963.
- [114] Wikipedia. "Space Mapping," [Online]. Available: https://en.wikipedia.org/wiki/Space_mapping
- [115] J. W. Bandler, R. M. Biernacki, S. H. Chen, and Y. F. Huang, "Design optimization of interdigital filters using aggressive space mapping and decomposition," *IEEE Trans. Microw. Theory Techn.*, vol. 45, no. 5, pp. 761–769, May 1997.
- [116] J. W. Bandler, R. M. Biernacki, S. H. Chen, and D. Omeragić, "Space mapping optimization of waveguide filters using finite element and mode-matching electromagnetic simulators," *Int. J. RF Microw. Comput.-Aided Eng.*, vol. 9, no. 1, pp. 54–70, Jan. 1999.
- [117] F. Wang and Q. J. Zhang, "Knowledge based neural models for microwave design," in *Proc. IEEE MTT-S Int. Microw. Symp.*, 1997, pp. 627–630.
- [118] F. Wang and Q. J. Zhang, "Incorporating combined functional/structural knowledge in library of neural models," in *Proc. IEEE Int. Joint Conf. Neural Netw. Proc. World Congr. Comput. Intell.*, 1998, pp. 363–366.
- [119] F. Wang, V. K. Devabhaktuni, and Q. J. Zhang, "A hierarchical neural network approach to the development of library of neural models for microwave design," in *Proc. IEEE MTT-S Int. Microw. Symp.*, 1998, pp. 1767–1770.
- [120] V. K. Devabhaktuni, C. Xi, and Q. J. Zhang, "A neural network approach to the modeling of heterojunction bipolar transistors from S-parameter data," in *Proc. 28th Eur. Microw. Conf.*, 1998, pp. 306–311.
- [121] J. W. Bandler, M. A. Ismail, J. E. Rayas-Sánchez, and Q. J. Zhang, "New directions in model development for RF/microwave components utilizing artificial neural networks and space mapping," in *IEEE Antennas Propag. Soc. Int. Symp. Dig.*, 1999, pp. 2572–2575.
- [122] J. W. Bandler, M. A. Ismail, J. E. Rayas-Sánchez, and Q. J. Zhang, "Neuromodeling of microwave circuits exploiting space mapping technology," *IEEE Trans. Microw. Theory Techn.*, vol. 47, no. 12, pp. 2417–2427, Dec. 1999.
- [123] J. E. Rayas-Sánchez, "Neural space mapping methods for modeling and design of microwave circuits," Ph.D. dissertation, Dept. Elect. Comp. Eng., McMaster Univ., Hamilton, ON, Canada, 2001.
- [124] L. Zhang, J. Xu, M. C. E. Yagoub, R. Ding, and Q. J. Zhang, "Neuro-space mapping technique for nonlinear device modeling and large signal simulation," in *Proc. IEEE MTT-S Int. Microw. Symp.*, 2003, pp. 173–176.
- [125] Y. Cao and G. Wang, "A wideband and scalable model of spiral inductors using space-mapping neural network," *IEEE Trans. Microw. Theory Techn.*, vol. 55, no. 12, pp. 2473–2480, Dec. 2007.
- [126] M. Simsek and N. S. Sengor, "A knowledge-based neuromodeling using space mapping technique: Compound space mapping-based neuromodeling," *Int. J. Numer. Modelling, Electron. Netw., Devices Fields*, vol. 21, no. 1/2, pp. 133–149, Jan.–Apr. 2008.
- [127] Y. Cao, L. Simonovich, and Q. J. Zhang, "A broadband and parametric model of differential via holes using space-mapping neural network," *IEEE Microw. Wireless Compon. Lett.*, vol. 19, no. 9, pp. 533–535, Sep. 2009.
- [128] Q. J. Zhang, J. Bandler, S. Koziel, H. Kabir, and L. Zhang, "ANN and space mapping for microwave modelling and optimization," in *Proc. IEEE MTT-S Int. Microw. Symp.*, 2010, pp. 980–983.
- [129] D. Gorissen, L. Zhang, Q. J. Zhang, and T. Dhaene, "Evolutionary neuro-space mapping technique for modeling of nonlinear microwave devices," *IEEE Trans. Microw. Theory Techn.*, vol. 59, no. 2, pp. 213–229, Feb. 2011.
- [130] X. Liu, G. Wang, and J. Liu, "Wideband model of on-chip CMOS interconnects using space-mapping technique," *Int. J. RF Microw. Comput.-Aided Eng.*, vol. 21, no. 4, pp. 439–445, Jul. 2011.
- [131] L. Zhu, K. Liu, Q. J. Zhang, Y. Ma, and B. Peng, "An enhanced analytical neuro-space mapping method for large-signal microwave device modeling," in *Proc. IEEE MTT-S Int. Microw. Symp.*, 2012, pp. 1–3.
- [132] J. E. Rayas-Sánchez and Q. J. Zhang, "On knowledge-based neural networks and neuro-space mapping," in *Proc. IEEE MTT-S Int. Microw. Symp.*, 2012, pp. 1–3.
- [133] S. Yan, S. Zhang, Y. Zhang, X. Dong, and W. Shi, "An accurate neuro-space mapping method for heterojunction bipolar transistor modeling," in *Proc. IEEE MTT-S Int. Conf. Numer. Electromagn. Multiphys. Model. Optim.*, 2020, pp. 1–4.
- [134] S. Yang, J. Zhou, C. Wu, X. Chen, L. Zhang, and E. P. Li, "A neuro-space mapping method for harmonic interference prediction of SOI/FET radio frequency switches," *IEEE Trans. Electromagn. Comput.*, vol. 64, no. 4, pp. 1117–1123, Aug. 2022.
- [135] Y. Wang, S. Li, and M. Yu, "Hybrid models for effective design and optimization of large-scale multiplexing networks," *IEEE Trans. Microw. Theory Techn.*, vol. 61, no. 5, pp. 1839–1849, May 2013.
- [136] J. V. Morro et al., "Automated design of waveguide filters using aggressive space mapping with a segmentation strategy and hybrid optimization techniques," in *Proc. IEEE MTT-S Int. Microw. Symp.*, 2003, pp. 1215–1218.
- [137] J. V. M. Ros et al., "Fast automated design of waveguide filters using aggressive space mapping with a new segmentation strategy and a hybrid optimization algorithm," *IEEE Trans. Microw. Theory Techn.*, vol. 53, no. 4, pp. 1130–1142, Apr. 2005.

- [138] J. V. Morro, H. Esteban, V. E. Boria, C. Bachiller, and A. Coves, "New multimodel aggressive space mapping technique for the efficient design of complex microwave circuits," in *Proc. IEEE MTT-S Int. Microw. Symp.*, 2005, pp. 1613–1616.
- [139] L. J. Roglá, J. E. Rayas-Sánchez, V. E. Boria, and J. Carbonell, "EM-based space mapping optimization of left-handed coplanar waveguide filters with split ring resonators," in *Proc. IEEE MTT-S Int. Microw. Symp.*, 2007, pp. 111–114.
- [140] A. Rodríguez, J. Selga, M. Gil, J. Carbonell, V. E. Boria, and F. Martín, "Automated synthesis of resonant-type metamaterial transmission lines using aggressive space mapping," in *Proc. IEEE MTT-S Int. Microw. Symp.*, 2010, pp. 209–212.
- [141] J. Selga, A. Rodríguez, V. E. Boria, and F. Martín, "Application of aggressive space mapping to the synthesis of composite right/left handed (CRLH) transmission lines based on complementary split ring resonators (CSRRLs)," in *Proc. 41st Eur. Microw. Conf.*, 2011, pp. 968–971.
- [142] J. Selga, A. Rodríguez, V. E. Boria, and F. Martín, "Synthesis of split-rings-based artificial transmission lines through a new two-step, fast converging, and robust aggressive space mapping (ASM) algorithm," *IEEE Trans. Microw. Theory Techn.*, vol. 61, no. 6, pp. 2295–2308, Jun. 2013.
- [143] J. Selga, M. Sans, J. Bonache, A. Rodríguez, V. E. Boria, and F. Martín, "Automated synthesis of planar wideband bandpass filters based on stepped impedance resonators (SIRs) and shunt stubs through aggressive space mapping (ASM)," in *Proc. IEEE MTT-S Int. Microw. Symp.*, 2014, pp. 1–4.
- [144] A. García-Lampérez and M. Salazar-Palma, "Multilevel aggressive space mapping applied to coupled-resonator filters," in *Proc. IEEE MTT-S Int. Microw. Symp.*, 2016, pp. 1–4.
- [145] J. Ossorio, J. C. Melgarejo, V. E. Boria, M. Guglielmi, and J. W. Bandler, "On the alignment of low-fidelity and high-fidelity simulation spaces for the design of microwave waveguide filters," *IEEE Trans. Microw. Theory Techn.*, vol. 66, no. 12, pp. 5183–5196, Dec. 2018.
- [146] J. C. Melgarejo, M. Guglielmi, S. Cogollos, and V. E. Boria, "An efficient microwave filter design procedure based on space mapping," in *Proc. 50th Eur. Microw. Conf.*, 2021, pp. 743–746.
- [147] M. A. Ismail, D. Smith, A. Panariello, Y. Wang, and M. Yu, "EM based design of large-scale dielectric resonator filters and multiplexers by space mapping," *IEEE Trans. Microw. Theory Techn.*, vol. 52, no. 1, pp. 386–392, Jan. 2004.
- [148] J. W. Bandler, M. A. Ismail, and J. E. Rayas-Sánchez, "Expanded space mapping EM based design framework exploiting preassigned parameters," *IEEE Trans. Circuits Syst. I, Fundam. Theory Appl.*, vol. 49, no. 12, pp. 1833–1838, Dec. 2002.
- [149] J. W. Bandler, Q. S. Cheng, N. K. Georgieva, and M. A. Ismail, "Implicit space mapping optimization exploiting preassigned parameters," *IEEE Trans. Microw. Theory Techn.*, vol. 52, no. 1, pp. 378–385, Jan. 2004.
- [150] S. Koziel, J. W. Bandler, and Q. S. Cheng, "Constrained parameter extraction for microwave design optimisation using implicit space mapping," *IET Microw. Antennas Propag.*, vol. 5, no. 5, pp. 1156–1163, Jul. 2011.
- [151] Q. S. Cheng, J. W. Bandler, and S. Koziel, "A review of implicit space mapping optimization and modeling techniques," in *Proc. IEEE MTT-S Int. Conf. Numer. Electromagn. Multiphys. Model. Optim.*, 2015, pp. 1–3.
- [152] S. Koziel, A. Bekasiewicz, and J. W. Bandler, "Implicit space mapping with variable-fidelity EM simulations and substrate partitioning for reliable microwave design optimization," in *Proc. IEEE MTT-S Int. Microw. Symp.*, 2017, pp. 548–550.
- [153] Q. S. Cheng, J. W. Bandler, and J. E. Rayas-Sánchez, "Tuning-aided implicit space mapping," *Int. J. RF Microw. CAE*, vol. 18, no. 5, pp. 445–453, Sep. 2008.
- [154] S. Koziel, J. Meng, J. W. Bandler, M. H. Bakr, and Q. S. Cheng, "Accelerated microwave design optimization with tuning space mapping," *IEEE Trans. Microw. Theory Techn.*, vol. 57, no. 2, pp. 383–394, Feb. 2009.
- [155] Q. S. Cheng, J. W. Bandler, and S. Koziel, "Space mapping design framework exploiting tuning elements," *IEEE Trans. Microw. Theory Techn.*, vol. 58, no. 1, pp. 136–144, Jan. 2010.
- [156] J. C. Rautio, "Shortening the design cycle," *IEEE Microw. Mag.*, vol. 9, no. 6, pp. 86–96, Dec. 2008.
- [157] Q. S. Cheng, J. C. Rautio, J. W. Bandler, and S. Koziel, "Progress in simulator-based tuning—The art of tuning space mapping [application notes]," *IEEE Microw. Mag.*, vol. 11, no. 4, pp. 96–110, Jun. 2010.
- [158] J. C. Rautio, "EM filter design success: The fast way," *Microw. J.*, vol. 58, Feb. 2015. [Online]. Available: <https://www.microwavejournal.com/articles/23813-em-filter-design-success-the-fast-way>
- [159] J. C. Rautio, "Tuning ports in the middle of resonators," in *Proc. IEEE MTT-S Int. Microw. Symp.*, 2017, pp. 1509–1511.
- [160] D. Swanson, "Port tuning a microstrip-folded hairpin filter [application notes]," *IEEE Microw. Mag.*, vol. 21, no. 4, pp. 18–28, Apr. 2020.
- [161] J. W. Bandler, Q. Cheng, D. H. Gebre-Mariam, K. Madsen, F. Pedersen, and J. Sondergaard, "EM-based surrogate modeling and design exploiting implicit, frequency and output space mappings," in *Proc. IEEE MTT-S Int. Microw. Symp.*, 2003, pp. 1003–1006.
- [162] J. E. Rayas-Sánchez and J. A. Jasso-Urzúa, "EM-based optimization of a single layer SIW with microstrip transitions using linear output space mapping," in *Proc. IEEE MTT-S Int. Microw. Symp.*, 2009, pp. 525–528.
- [163] S. Koziel, J. W. Bandler, and K. Madsen, "Space mapping with adaptive response correction for microwave design optimization," *IEEE Trans. Microw. Theory Techn.*, vol. 57, no. 2, pp. 478–486, Feb. 2009.
- [164] D. Echeverría and P. W. Hemker, "Space mapping and defect correction," *Comput. Methods Appl. Math.*, vol. 5, no. 2, pp. 107–136, 2005.
- [165] P. W. Hemker and D. Echeverría, "A trust-region strategy for manifold-mapping optimization," *J. Comput. Phys.*, vol. 224, no. 1, pp. 464–475, Apr. 2007.
- [166] L. Encica, J. J. H. Paulides, E. A. Lomonova, and A. J. A. Vandeput, "Aggressive output space-mapping optimization for electromagnetic actuators," *IEEE Trans. Magn.*, vol. 44, no. 6, pp. 1106–1109, Jun. 2008.
- [167] J. Gong, F. Gillon, J. T. Canh, and Y. Xu, "Proposal of a kriging output space mapping technique for electromagnetic design optimization," *IEEE Trans. Magn.*, vol. 53, no. 6, Jun. 2017, Art no. 8106004.
- [168] J. W. Bandler, M. A. Ismail, J. E. Rayas-Sánchez, and Q. J. Zhang, "Neural inverse space mapping EM-optimization," in *Proc. IEEE MTT-S Int. Microw. Symp.*, 2001, pp. 1007–1010.
- [169] J. W. Bandler, M. A. Ismail, J. E. Rayas-Sánchez, and Q. J. Zhang, "Neural inverse space mapping (NISM) for EM-based microwave design," *Int. J. RF Microw. CAE*, vol. 13, no. 2, pp. 136–147, Mar. 2003.
- [170] J. E. Rayas-Sánchez, F. Lara-Rojo, and E. Martínez-Guerrero, "A linear inverse space mapping algorithm for microwave design in the frequency and transient domains," in *Proc. IEEE MTT-S Int. Microw. Symp.*, 2004, pp. 1847–1850.
- [171] J. E. Rayas-Sánchez, F. Lara-Rojo, and E. Martínez-Guerrero, "A linear inverse space mapping (LISM) algorithm to design linear and nonlinear RF and microwave circuits," *IEEE Trans. Microw. Theory Techn.*, vol. 53, no. 3, pp. 960–968, Mar. 2005.
- [172] M. Simsek and N. S. Sengor, "Solving inverse problems by space mapping with inverse difference method," in *Scientific Computing in Electrical Engineering SCEE 2008*, vol. 14, J. Roos and L. Costa, Eds. Berlin, Germany: Springer-Verlag, 2010, pp. 453–460.
- [173] J. Hinojosa, F. D. Quesada Pereira, M. Martínez-Mendoza, and A. Alvarez-Melcón, "Optimization-oriented design of RF/microwave circuits using inverse-linear-input neuro-fuzzy-output space mapping with two different dimensionality simulators," *IEEE Trans. Circuits Syst. I*, vol. 58, no. 1, pp. 176–185, Jan. 2011.
- [174] J. E. Rayas-Sánchez and N. Vargas-Chávez, "A linear regression inverse space mapping algorithm for EM-based design optimization of microwave circuits," in *Proc. IEEE MTT-S Int. Microw. Symp.*, 2011, pp. 1–4.
- [175] J. W. Bandler, J. E. Rayas-Sánchez, and Q. J. Zhang, "Yield-driven electromagnetic optimization via space mapping-based neuromodels," *Int. J. RF Microw. CAE*, vol. 12, no. 1, pp. 79–89, Jan. 2002.
- [176] S. Koziel, J. W. Bandler, A. S. Mohamed, and K. Madsen, "Enhanced surrogate models for statistical design exploiting space mapping technology," in *Proc. IEEE MTT-S Int. Microw. Symp.*, 2005, pp. 1609–1612.
- [177] L. Zhang, Q. J. Zhang, and J. Wood, "Statistical neuro-space mapping technique for large-signal modeling of nonlinear devices," *IEEE Trans. Microw. Theory Techn.*, vol. 56, no. 11, pp. 2453–2467, Nov. 2008.
- [178] Q. S. Cheng, J. W. Bandler, N. K. Nikolova, and S. Koziel, "A statistical input space mapping approach for accommodating modeling residuals," in *Proc. IEEE MTT-S Int. Microw. Symp.*, 2013, pp. 1–3.

- [179] J. Zhang, F. Feng, W. Na, S. Yan, and Q. J. Zhang, "Parallel space-mapping based yield-driven EM optimization incorporating trust region algorithm and polynomial chaos expansion," *IEEE Access*, vol. 7, pp. 143673–143683, 2019.
- [180] J. Zhang, F. Feng, J. Jin, and Q. J. Zhang, "Efficient yield estimation of microwave structures using mesh deformation-incorporated space mapping surrogates," *IEEE Microw. Wireless Compon. Lett.*, vol. 30, no. 10, pp. 937–940, Oct. 2020.
- [181] J. Zhang, F. Feng, and Q. Zhang, "Efficient yield optimization of microwave circuits using space mapping surrogates and parallel data generation," in *Proc. IEEE MTT-S Int. Conf. Numer. Electromagn. Multiphys. Model. Optim.*, 2020, pp. 1–4.
- [182] M. Brumos, V. E. Boria, M. Guglielmi, and S. Cogollos, "Correction of manufacturing deviations in circular-waveguide dual-mode filters using aggressive space mapping," in *Proc. Eur. Microw. Conf.*, 2014, pp. 624–627.
- [183] S. Cogollos et al., "Correction of manufacturing deviations in waveguide filters and manifold multiplexers using metal insertions," *Int. J. Microw. Wireless Technol.*, vol. 7, no. 3/4, pp. 219–227, Jun. 2015.
- [184] A. Berenguer, M. Baquero-Escudero, M. Ferrando-Rocher, B. Bernardo-Clemente, and V. E. Boria, "An effective post-manufactured tuning method for gap waveguide components," in *Proc. IEEE Int. Symp. Antennas Propag.*, 2016, pp. 493–494.
- [185] J. C. Melgarejo, J. Ossorio, A. A. San-Blas, M. Guglielmi, and V. E. Boria, "Space mapping filter design and tuning techniques," *Int. J. Microw. Wireless Technol.*, vol. 14, no. 3, pp. 387–396, 2016.
- [186] A. Rodriguez et al., "Robust optimization and tuning of microwave filters and artificial transmission lines using aggressive space mapping techniques," in *Proc. IEEE MTT-S Int. Microw. Symp.*, 2017, pp. 1501–1504.
- [187] S. Li, X. Fan, P. D. Laforge, and Q. S. Cheng, "Space mapping post-fabrication tuning of 3D printed air-filled waveguide filter," in *Proc. IEEE MTT-S Int. Microw. Symp.*, 2017, pp. 1635–1637.
- [188] S. Li, Q. S. Cheng, X. Fan, G. I. Huang, and P. Laforge, "Frequency-dependent implicit space-mapping algorithm for wideband microwave prototyping," in *Proc. IEEE Int. Conf. Comput. Electromagn.*, 2018, pp. 1–3.
- [189] J. C. Melgarejo, J. Ossorio, S. Cogollos, M. Guglielmi, V. E. Boria, and J. W. Bandler, "On space mapping techniques for microwave filter tuning," *IEEE Trans. Microw. Theory Techn.*, vol. 67, no. 12, pp. 4860–4870, Dec. 2019.
- [190] S. Li, X. Fan, P. D. Laforge, and Q. S. Cheng, "Surrogate model-based space mapping in postfabrication bandpass filters' tuning," *IEEE Trans. Microw. Theory Techn.*, vol. 68, no. 6, pp. 2172–2182, Jun. 2020.
- [191] Y. Yu, Z. Zhang, Q. S. Cheng, B. Liu, and Y. Wang, "Surrogate-based design and tuning methods for RF/microwave devices," in *Proc. IEEE MTT-S Int. Microw. Symp.*, 2022, pp. 111–114.
- [192] J. C. Melgarejo, J. Ossorio, D. Rubio, S. Cogollos, M. Guglielmi, and V. E. Boria, "Recent advances on aggressive space mapping techniques for waveguide filters design and tuning," in *Proc. IEEE MTT-S Int. Microw. Symp.*, 2022, pp. 121–124.
- [193] F. E. Rangel-Patiño, J. L. Chávez-Hurtado, A. Viveros-Wacher, J. E. Rayas-Sánchez, and N. Hakim, "System margining surrogate-based optimization in post-silicon validation," *IEEE Trans. Microw. Theory Techn.*, vol. 65, no. 9, pp. 3109–3115, Sep. 2017.
- [194] F. E. Rangel-Patiño, J. E. Rayas-Sánchez, A. Viveros-Wacher, E. A. Vega-Ochoa, and N. Hakim, "High-speed links receiver optimization in post-silicon validation exploiting Broyden-based input space mapping," in *Proc. IEEE MTT-S Int. Conf. Numer. Electromagn. Multiphys. Model. Optim.*, 2018, pp. 1–3.
- [195] F. E. Rangel-Patiño, J. E. Rayas-Sánchez, and N. Hakim, "Transmitter and receiver equalizers optimization methodologies for high-speed links in industrial computer platforms post-silicon validation," in *Proc. IEEE Int. Test Conf.*, 2018, pp. 1–10.
- [196] J. E. Rayas-Sánchez, F. E. Rangel-Patiño, B. Mercado-Casillas, F. Leal-Romo, and J. L. Chávez-Hurtado, "Machine learning techniques and space mapping approaches to enhance signal and power integrity in high-speed links and power delivery networks," in *Proc. IEEE 11th Latin Amer. Symp. Circuit Syst.*, 2020, pp. 1–4.
- [197] J. E. Rayas-Sánchez and J. W. Bandler, "System-level measurement-based design optimization by space mapping technology," in *Proc. IEEE MTT-S Int. Microw. Symp.*, 2022, pp. 118–120.
- [198] *1st Int. Workshop on Surrogate Modelling and Space Mapping for Engineering Optimization (SMSMEO 2000)*, Technical Univ. Denmark, Lyngby, Denmark, Nov. 2000. [Online]. Available: <https://web.archive.org/web/20000816214543/http://www.imm.dtu.dk/~km/smsmeo>
- [199] *2nd International Workshop on Surrogate Modelling and Space Mapping for Engineering Optimization*, Technical Univ. Denmark, Lyngby, Denmark, Nov. 2006. [Online]. Available: <https://web.archive.org/web/20070623212855/http://www2.imm.dtu.dk/km/~smsmeo-06/>
- [200] *3rd International Workshop on Surrogate Modelling and Space Mapping for Engineering Optimization (SMSMEO 2012)*, Reykjavik Univ., Reykjavik, Iceland, Aug. 2012. [Online]. Available: <https://web.archive.org/web/20120325055035/http://eomc.ru.is/smsmeo2012/>
- [201] J. W. Bandler, R. M. Biernacki, S. H. Chen, and D. Omeragić, "Space mapping optimization of waveguide filters using finite element and mode-matching electromagnetic simulators," in *Proc. IEEE MTT-S Int. Microw. Symp.*, 1997, pp. 635–638.
- [202] S. Koziel, "Surrogate-based optimization of microwave structures using space mapping and kriging," in *Proc. Eur. Microw. Conf.*, 2009, pp. 1062–1065.
- [203] S. Koziel, I. Couckuyt, and T. Dhaene, "Reliable low-cost co-kriging modeling of microwave devices," in *Proc. IEEE MTT-S Int. Microw. Symp.*, 2012, pp. 1–3.
- [204] S. Koziel, J. W. Bandler, and K. Madsen, "A space mapping framework for engineering optimization—Theory and implementation," *IEEE Trans. Microw. Theory Techn.*, vol. 54, no. 10, pp. 3721–3730, Oct. 2006.
- [205] S. Koziel and J. W. Bandler, "Space mapping algorithm with improved convergence properties for microwave optimization," in *Proc. Eur. Microw. Integr. Circuit. Conf.*, 2008, pp. 310–313.
- [206] S. Koziel, J. W. Bandler, and Q. S. Cheng, "Trust-region-based convergence safeguards for space mapping design optimization of microwave circuits," in *Proc. IEEE MTT-S Int. Microw. Symp.*, 2009, pp. 1261–1264.
- [207] S. Koziel and D. Echeverría-Ciaurri, "Reliable simulation-driven design optimization of microwave structures using manifold mapping," *Prog. Electromagn. Res. B*, vol. 26, pp. 361–382, 2010.
- [208] S. Koziel, "Space mapping: Performance, reliability, open problems and perspectives," in *Proc. IEEE MTT-S Int. Microw. Symp.*, 2017, pp. 1512–1514.
- [209] S. Koziel and A. Bekasiewicz, "Fast EM-driven optimization using variable-fidelity EM models and adjoint sensitivities," *IEEE Microw. Wireless Compon. Lett.*, vol. 26, no. 2, pp. 80–82, Feb. 2016.
- [210] S. Koziel and A. Pietrenko-Dabrowska, "Variable-fidelity simulation models and sparse gradient updates for cost-efficient optimization of compact antenna input characteristics," *Sensors*, vol. 19, no. 8, Apr. 2019, Art. no. e1806.
- [211] S. Koziel, A. Pietrenko-Dabrowska, and P. Plotka, "Reduced-cost microwave design closure by multi-resolution em simulations and knowledge-based model management," *IEEE Access*, vol. 9, pp. 116326–116337, 2021.
- [212] W. Zhang et al., "Surrogate-assisted multistate tuning-driven EM optimization for microwave tunable filter," *IEEE Trans. Microw. Theory Techn.*, vol. 70, no. 4, pp. 2015–2030, Apr. 2022.
- [213] Y. Yu et al., "State-of-the-art: AI-assisted surrogate modeling and optimization for microwave filters," *IEEE Trans. Microw. Theory Techn.*, vol. 70, no. 11, pp. 4635–4651, Nov. 2022.
- [214] L. Leifsson, X. Du, and S. Koziel, "Efficient yield estimation of multi-band patch antennas by polynomial chaos-based kriging," *Int. J. Numer. Modelling*, vol. 33, no. 6, Jan. 2020, Art. no. e2722.
- [215] J. Zhang, F. Feng, and Q. J. Zhang, "Rapid yield estimation of microwave passive components using model-order reduction based neuro-transfer function models," *IEEE Microw. Wireless Compon. Lett.*, vol. 31, no. 4, pp. 333–336, Apr. 2021.
- [216] S. Koziel and S. Ogurtsov, "Multi-objective design of antennas using variable-fidelity simulations and surrogate models," *IEEE Trans. Antennas Propag.*, vol. 61, no. 12, pp. 5931–5939, Dec. 2013.

- [217] S. Koziel and A. Bekasiewicz, *Multi-Objective Design of Antennas using Surrogate Models*. Singapore: World Scientific, 2016.
- [218] S. Koziel and A. Bekasiewicz, "Rapid simulation-driven multi-objective design optimization of decomposable compact microwave passives," *IEEE Trans. Microw. Theory Techn.*, vol. 64, no. 8, pp. 2454–2461, Aug. 2016.
- [219] S. Koziel and A. Bekasiewicz, "Comprehensive comparison of compact UWB antenna performance by means of multiobjective optimization," *IEEE Trans. Antennas Propag.*, vol. 65, no. 7, pp. 3427–3436, Jul. 2017.
- [220] S. Koziel and P. Kurgan, "Rapid multi-objective design of integrated on-chip inductors by means of Pareto front exploration and design extrapolation," *Int. J. Electromagn. Waves Appl.*, vol. 33, no. 11, pp. 1416–1426, Apr. 2019.
- [221] S. Koziel and A. Bekasiewicz, "Expedited geometry scaling of compact microwave passives by means of inverse surrogate modeling," *IEEE Trans. Microw. Theory Techn.*, vol. 63, no. 12, pp. 4019–4026, Dec. 2015.
- [222] S. Koziel, A. Bekasiewicz, and L. Leifsson, "Rapid EM-driven antenna dimension scaling through inverse modeling," *IEEE Antennas Wireless Propag. Lett.*, vol. 15, pp. 714–717, 2016.
- [223] S. Koziel and P. Kurgan, "Compact cell topology selection for size-reduction-oriented design of microstrip rat-race couplers," *Int. J. RF Microw. CAE*, vol. 28, no. 5, Jun. 2018, Art. no. e21261.
- [224] A. Bekasiewicz, P. Kurgan, and S. Koziel, "Numerically efficient miniaturization-oriented optimization of an ultra-wideband spline-parameterized antenna," *IEEE Access*, vol. 10, pp. 21608–21618, 2022.
- [225] A. Pietrenko-Dabrowska and S. Koziel, "Reliable surrogate modeling of antenna input characteristics by means of domain confinement and principal components," *Electronics*, vol. 9, no. 5, May 2020, Art. no. e877.
- [226] S. Koziel, A. Pietrenko-Dabrowska, and M. Mahrokh, "Globalized simulation-driven miniaturization of microwave circuits by means of dimensionality-reduced constrained surrogates," *Sci. Rep.*, vol. 12, no. 1, pp. 1–17, Sep. 2022.
- [227] S. Koziel, A. Pietrenko-Dabrowska, and U. Ullah, "Tolerance-aware optimization of microwave circuits by means of principal directions and domain-restricted metamodelling," *IEEE Trans. Microw. Theory Techn.*, vol. 70, no. 9, pp. 4085–4093, Sep. 2022.
- [228] S. Koziel, "Fast simulation-driven antenna design using response-feature surrogates," *Int. J. RF Microw. Comput.-Aided Eng.*, vol. 25, no. 5, pp. 394–402, Jun. 2015.
- [229] S. Koziel, Q. S. Cheng, and J. W. Bandler, "Feature-based surrogates for low-cost microwave modelling and optimisation," *IET Microw. Antennas Propag.*, vol. 9, no. 15, pp. 1706–1712, Sep. 2015.
- [230] S. Koziel and A. Bekasiewicz, "Simulation-driven size-reduction-oriented design of multi-band antennas by means of response features," *IET Microw. Antennas Propag.*, vol. 12, no. 7, pp. 1093–1098, Jun. 2018.
- [231] F. Feng et al., "Multifeature-assisted neuro-transfer function surrogate-based EM optimization exploiting trust-region algorithms for microwave filter design," *IEEE Trans. Microw. Theory Techn.*, vol. 68, no. 2, pp. 531–542, Feb. 2020.
- [232] S. Koziel and A. Pietrenko-Dabrowska, "Expedited feature-based quasi-global optimization of multi-band antennas with Jacobian variability tracking," *IEEE Access*, vol. 8, pp. 83907–83915, 2020.
- [233] S. Koziel and J. W. Bandler, "Rapid yield estimation and optimization of microwave structures exploiting feature-based statistical analysis," *IEEE Trans. Microw. Theory Techn.*, vol. 63, no. 1, pp. 107–114, Jan. 2015.
- [234] S. Koziel, A. Bekasiewicz, and Q. S. Cheng, "Response features for low-cost statistical analysis and tolerance-aware design of antennas," *Int. J. Numer. Modelling*, vol. 31, no. 3, May/June 2018, Art. no. e2297.
- [235] A. Pietrenko-Dabrowska and S. Koziel, "Design centering of compact microwave components using response features and trust regions," *Energies*, vol. 14, no. 24, Dec. 2021, Art. no. e8550.
- [236] J. Zhang, F. Feng, J. Jin, W. Zhang, Z. Zhao, and Q.-J. Zhang, "Adaptively weighted yield driven EM optimization incorporating neurotransfer function surrogate with applications to microwave filters," *IEEE Trans. Microw. Theory Techn.*, vol. 69, no. 1, pp. 518–528, Jan. 2021.
- [237] Z. Zhang, B. Liu, Y. Yu, and Q. S. Cheng, "A microwave filter yield optimization method based on off-line surrogate model-assisted evolutionary algorithm," *IEEE Trans. Microw. Theory Techn.*, vol. 70, no. 6, pp. 2925–2934, Jun. 2022.
- [238] A. Pietrenko-Dabrowska, S. Koziel, J. W. Bandler, and J. E. Rayas-Sánchez, "EM-driven tolerance optimization of compact microwave components using response feature surrogates," in *Proc. IEEE MTT-S Int. Microw. Symp.*, 2022, pp. 107–110.
- [239] S. Koziel and A. Pietrenko-Dabrowska, "Tolerance-aware multi-objective optimization of antennas by means of feature-based regression surrogates," *IEEE Trans. Antennas Propag.*, vol. 70, no. 7, pp. 5636–5646, Jul. 2022.
- [240] S. Koziel and A. Pietrenko-Dabrowska, "Tolerance optimization of antenna structures by means of response feature surrogates," *IEEE Trans. Antennas Propag.*, vol. 70, no. 11, pp. 10988–10997, Nov. 2022.
- [241] B. Shahriari, K. Swersky, Z. Wang, R. P. Adams, and N. de Freitas, "Taking the human out of the loop: A review of Bayesian optimization," *Proc. IEEE*, vol. 104, no. 1, pp. 148–175, Jan. 2016.
- [242] P. Chen, B. M. Merrick, and T. J. Brazil, "Bayesian optimization for broadband high-efficiency power amplifier designs," *IEEE Trans. Microw. Theory Techn.*, vol. 63, no. 12, pp. 4263–4272, Dec. 2015.
- [243] N. Knudde et al., "Data-efficient Bayesian optimization with constraints for power amplifier design," in *Proc. IEEE MTT-S Int. Conf. Numer. Electromagn. Multiphys. Model. Optim.*, 2018, pp. 1–3.
- [244] H. M. Torun, M. Larbi, and M. Swaminathan, "A Bayesian framework for optimizing interconnects in high-speed channels," in *Proc. IEEE MTT-S Int. Conf. Numer. Electromagn. Multiphys. Model. Optim.*, 2018, pp. 1–4.
- [245] D. Lho et al., "Bayesian optimization of high-speed channel for signal integrity analysis," in *Proc. IEEE Conf. Elect. Perform. Electron. Packag. Syst.*, 2019, pp. 1–3.
- [246] M. A. Dolatsara and M. Swaminathan, "Determining worst-case eye height in low BER channels using Bayesian optimization," in *Proc. IEEE Latin Amer. Symp. Circuits Syst.*, 2020, pp. 1–4.
- [247] X. Yang, J. Tang, H. M. Torun, W. D. Becker, J. A. Hejase, and M. Swaminathan, "Rx equalization for a high-speed channel based on Bayesian active learning using dropout," in *Proc. IEEE 29th Conf. Elect. Perform. Electron. Packag. Syst.*, 2020, pp. 1–3.
- [248] H. M. Torun and M. Swaminathan, "High-dimensional global optimization method for high-frequency electronic design," *IEEE Trans. Microw. Theory Techn.*, vol. 67, no. 6, pp. 2128–2142, Jun. 2019.
- [249] W. Na, K. Liu, H. Cai, W. Zhang, H. Xie, and D. Jin, "Efficient EM optimization exploiting parallel local sampling strategy and Bayesian optimization for microwave applications," *IEEE Microw. Wireless Compon. Lett.*, vol. 31, no. 10, pp. 1103–1106, Oct. 2021.
- [250] J. Huang, C. Tao, F. Yang, C. Yan, D. Zhou, and X. Zeng, "Bayesian optimization approach for RF circuit synthesis via multitask neural network enhanced Gaussian process," *IEEE Trans. Microw. Theory Techn.*, vol. 70, no. 11, pp. 4787–4795, Nov. 2022.
- [251] D. Kahneman, *Thinking, Fast and Slow*. New York, NY, USA: Farrar, Straus, 2012.
- [252] C. Zhang, F. Feng, V.-M.-R. Gongal-Reddy, Q. J. Zhang, and J. W. Bandler, "Cognition-driven formulation of space mapping for equal-ripple optimization of microwave filters," *IEEE Trans. Microw. Theory Techn.*, vol. 63, no. 7, pp. 2154–2165, Jul. 2015.
- [253] J. W. Bandler and Q. S. Cheng, "A brief history of tuning: From design centering to space mapping," in *Proc. IEEE WFI: EM-Based Tuning Techn., Comput.-Aided Tuning Tuning Space Mapping, MTT-S Int. Microw. Symp.*, 2015.
- [254] J. W. Bandler, *The Caffeine Rabbit Hole*, Dundas, ON, Canada: Bandler Corp., 2021. [Online]. Available: <http://www.bandler.com/rabbit>.
- [255] J. W. Bandler and Q. S. Cheng, "The essence of space mapping: Less is more," in *Proc. 3rd Int. Workshop Surrogate Modelling Space Mapping Eng. Optim.*, 2012.



JOHN W. BANDLER (Life Fellow, IEEE) studied at Imperial College London, London, U.K., and received the B.Sc.(Eng.), Ph.D., and D.Sc.(Eng.) degrees from the University of London, London, U.K., in 1963, 1967, and 1976, respectively. After one year with Mullard Research Laboratories, Redhill, U.K., and a two-year Postdoctoral Fellowship with the University of Manitoba, Winnipeg, MB, Canada, he joined McMaster University, Hamilton, ON, Canada, in 1969, where he is currently a Professor Emeritus. He founded Optimization Systems Associates Inc. in 1983 and sold it to Hewlett-Packard in 1997. He is the President of Bandler Corporation, Dundas, ON, Canada. He pioneered space mapping in 1993. He has authored fiction and nonfiction, and has written, produced and directed plays and a short film. He has given workshops on entrepreneurship, ethics, awareness, creativity, and creative thinking, and, across all disciplines, he coaches and mentors students and young professionals in communicating their research to general audiences. Dr. Bandler is a Fellow of several societies, including the Royal Society of Canada, and the Canadian Academy of Engineering. He was the recipient of the IEEE MTT-S Microwave Application Award in 2004, IEEE Canada McNaughton Gold Medal and the Queen Elizabeth II Diamond Jubilee Medal in 2012, IEEE MTT-S Microwave Career Award in 2013, McMaster University's Faculty of Engineering Research Achievement Award in 2014, appointment to Officer of the Order of Canada in 2016, McMaster's Lifetime Innovator Award and Professional Engineers Ontario's Gold Medal in 2018, and the 2023 IEEE Electromagnetics Award "For contributions to electromagnetic optimization and the modeling of high-frequency structures, circuits, and devices." From 2017 to 2022, he spearheaded and co-organized the IEEE International Microwave Symposium and Microwave Week Three Minute Thesis (3MT) competitions.



JOSÉ ERNESTO RAYAS-SÁNCHEZ (Senior Member, IEEE) was born in Guadalajara, Jalisco, Mexico, on December 27, 1961. He received the B.Sc. degree in electronics engineering from the ITESO, Guadalajara, Mexico, in 1984, the M.Sc. degree in electrical engineering from the ITESM, Monterrey, Mexico, in 1989, and the Ph.D. degree in electrical engineering from McMaster University, Hamilton, ON, Canada, in 2001. He is currently a *Profesor Numerario* (Hons.) with ITESO—The Jesuit University of

Guadalajara, Tlaquepaque, Mexico, where he leads the Research Group on Computer-Aided Engineering of Circuits and Systems. He is a member of the Mexican National System of Researchers, Level II. His research interests include computer-aided and knowledge-based modeling and design optimization of high-frequency circuits. Since 2019, Dr. Rayas-Sánchez has been the IEEE Microwave Theory and Techniques Society Representative of the IEEE EDA Council. He was the Chair during 2018–2019, and Vice-Chair during 2016–2017 of the MTT-S Technical Committee on Computer-Aided Design (formerly MTT-1, now MTT-2 on Design Automation). He is a member of the Technical Program Reviewers Committee of the IEEE MTT-S International Microwave Symposium). Since 2013, he has been the IEEE MTT-S Regional Coordinator for Latin America. He was the General Chair of the First IEEE MTT-S International Microwave Workshop Series in Region 9 (IMWS2009-R9) on Signal Integrity and High-Speed Interconnects, Guadalajara, Mexico, in February 2009, and General Chair of the First IEEE MTT-S Latin America Microwave Conference, Puerto Vallarta, Mexico, in December 2016. During 2016–2024, he is also an elected AdCom Member of the IEEE MTT-S.